

MODEL NAME : *PALB0*

PCB NO : *LA-6801P (DAB00000410)*

BOM P/N : *46198531L01 -->R3*

46198531L02 -->R1

46198531L03 -->R3

46198531L04 -->R1

Dell/Compal Confidential

Schematic Document

Specter (Huron River)

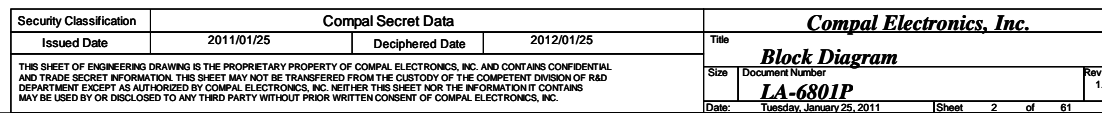
Sandy Bridge(PGA) + Cougar Point(standard)

DISCRETE VGA N12E-GE-B (optimus)

2011-01-25

Rev: 1.0

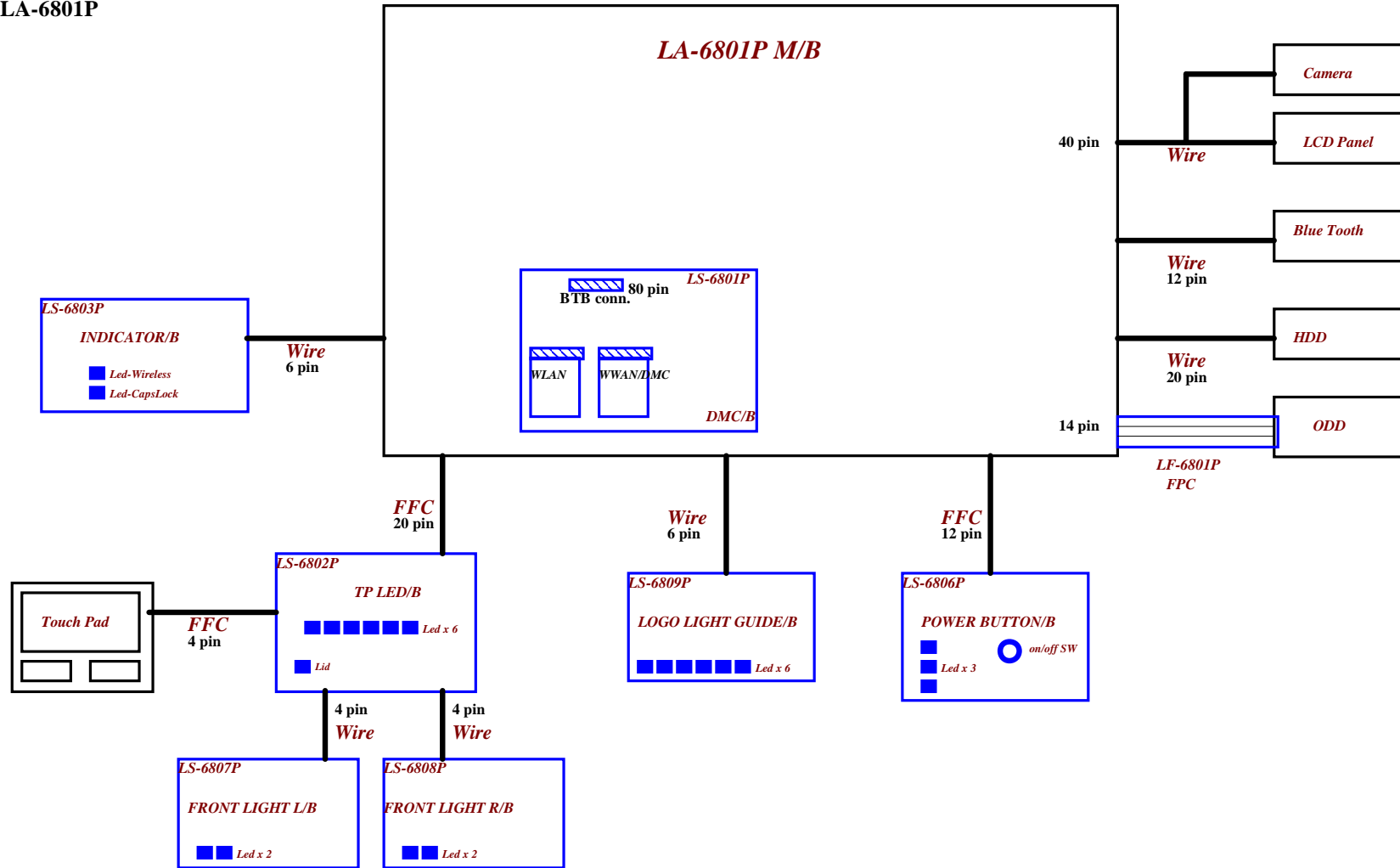
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Project Code : PALB0

File Name : LA-6801P



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Board ID Table for AD channel

Vcc	3.3V +/- 5%				
Ra	100K +/- 5%				
Board ID	Rb	VAD_BID_min	VAD_BID_typ	VAD_BID_max	EC_AD3
0	0	0 V	0 V	0.155 V	0x00-0x0C
1	8.2K +/- 5%	0.168 V	0.250 V	0.362 V	0x0D-0x1C
2	18K +/- 5%	0.375 V	0.503 V	0.621 V	0x1D-0x30
3	33K +/- 5%	0.634 V	0.819 V	0.945 V	0x31-0x49
4	56K +/- 5%	0.958 V	1.185 V	1.359 V	0x4A-0x69
5	100K +/- 5%	1.372 V	1.650 V	1.838 V	0x6A-0x8E
6	200K +/- 5%	1.851 V	2.200 V	2.420 V	0x8F-0xBB
7	NC	2.433 V	3.300 V	3.300 V	0xBC-0xFF

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	0.4
4	1.0
5	
6	
7	

SMBUS Control Table

	SOURCE	MINI1	MINI2	BATT	SODIMM	Thermal Sensor 1	Thermal Sensor 2	FFS	VGA Thermal Sensor	VGA	DMC	XDP	Charger
EC_SMB_CK1 EC_SMB_DA1	KB930			V									
EC_SMB_CK2 EC_SMB_DA2	KB930					V	V		V				
PCH_SML0CLK PCH_SML0DATA	PCH												
PCH_SML1CLK PCH_SML1DATA	PCH												V
MEM_SMBCLK MEM_SMBDATA	PCH	V	V		V			V		V	V	V	

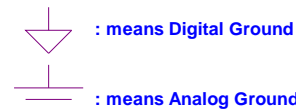
Link

CLKOUT	DESTINATION
PCI0	PCH_LOOPBACK
PCI1	EC LPC
PCI2	None
PCI3	None
PCI4	None

CLK	DIFFERENTIAL	DESTINATION	FLEX CLOCKS	DESTINATION
	CLKOUT_PCIE0	None	CLKOUTFLEX0	None
	CLKOUT_PCIE1	10/100/1G LAN	CLKOUTFLEX1	None
	CLKOUT_PCIE2	MINI CARD-2 WWAN	CLKOUTFLEX2	None
	CLKOUT_PCIE3	MINI CARD-1 WLAN	CLKOUTFLEX3	None
	CLKOUT_PCIE4	CARD READER		
	CLKOUT_PCIE5	None		
	CLKOUT_PCIE6	USB 3.0		
	CLKOUT_PCIE7	None		
	CLKOUT_PEG_B	None		

SATA	DESTINATION
SATA0	HDD
SATA1	None
SATA2	ODD
SATA3	None
SATA4	None
SATA5	None

Symbol Note :

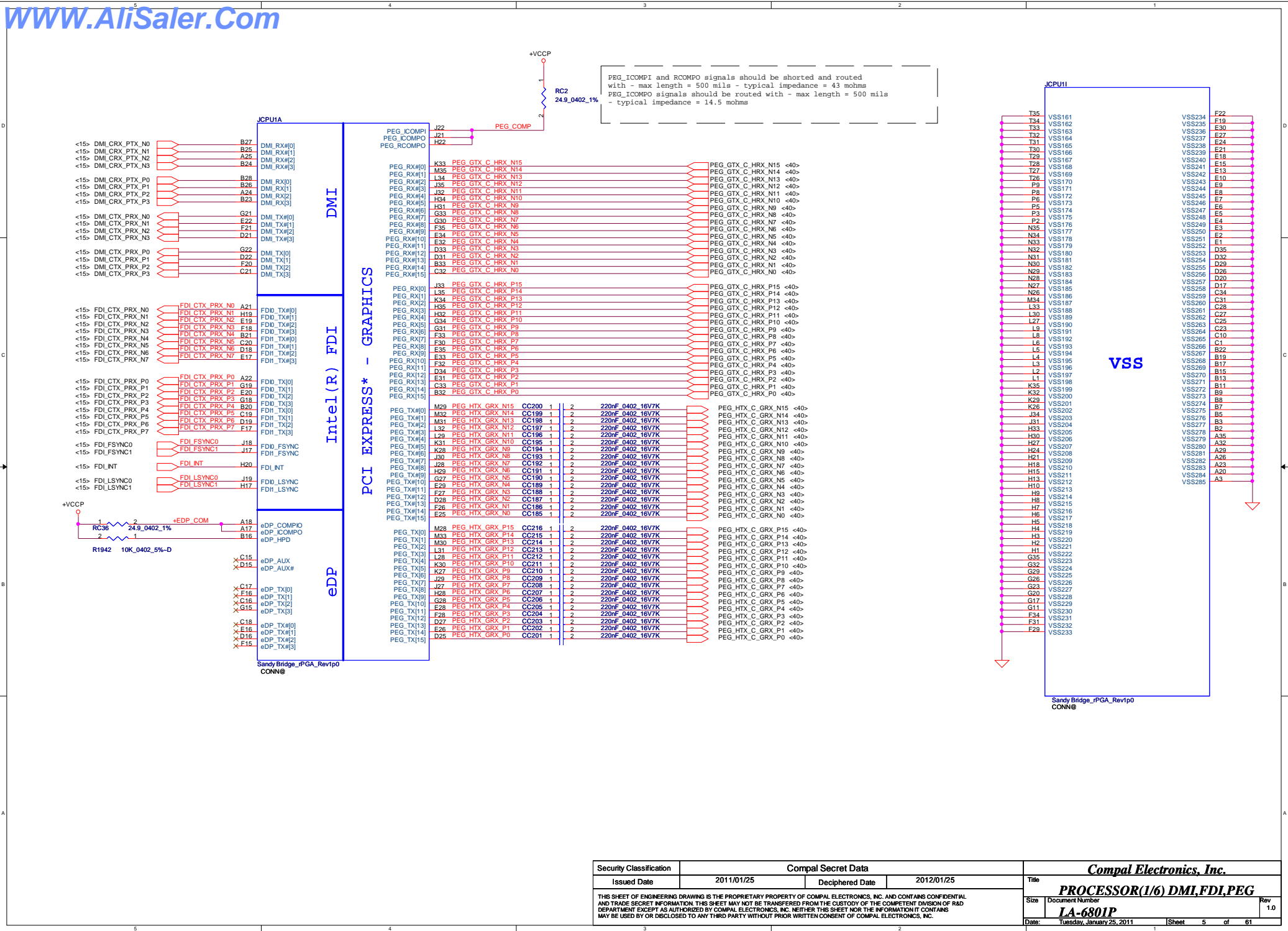


PCI EXPRESS	DESTINATION
Lane 1	10/100/1G LAN
Lane 2	MINI CARD-2 WWAN/DMC
Lane 3	MINI CARD-1 WLAN
Lane 4	CARD READER
Lane 5	None
Lane 6	USB 3.0
Lane 7	None
Lane 8	None

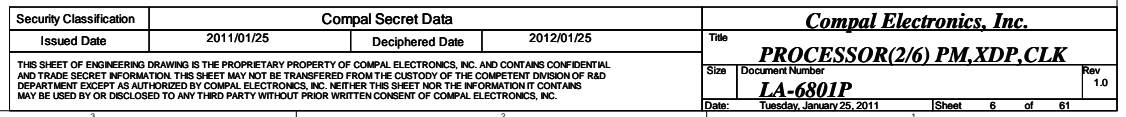
PCH

USB PORT#	DESTINATION
0	None
1	JUSB1 (2.0 Ext Left Side)
2	Bluetooth
3	CAMERA
4	JMINI1 (WLAN)
5	JMINI2 (WWAN/DMC)
6	ELC 8051
7	None
8	None
9	None
10	None
11	None
12	None
13	None

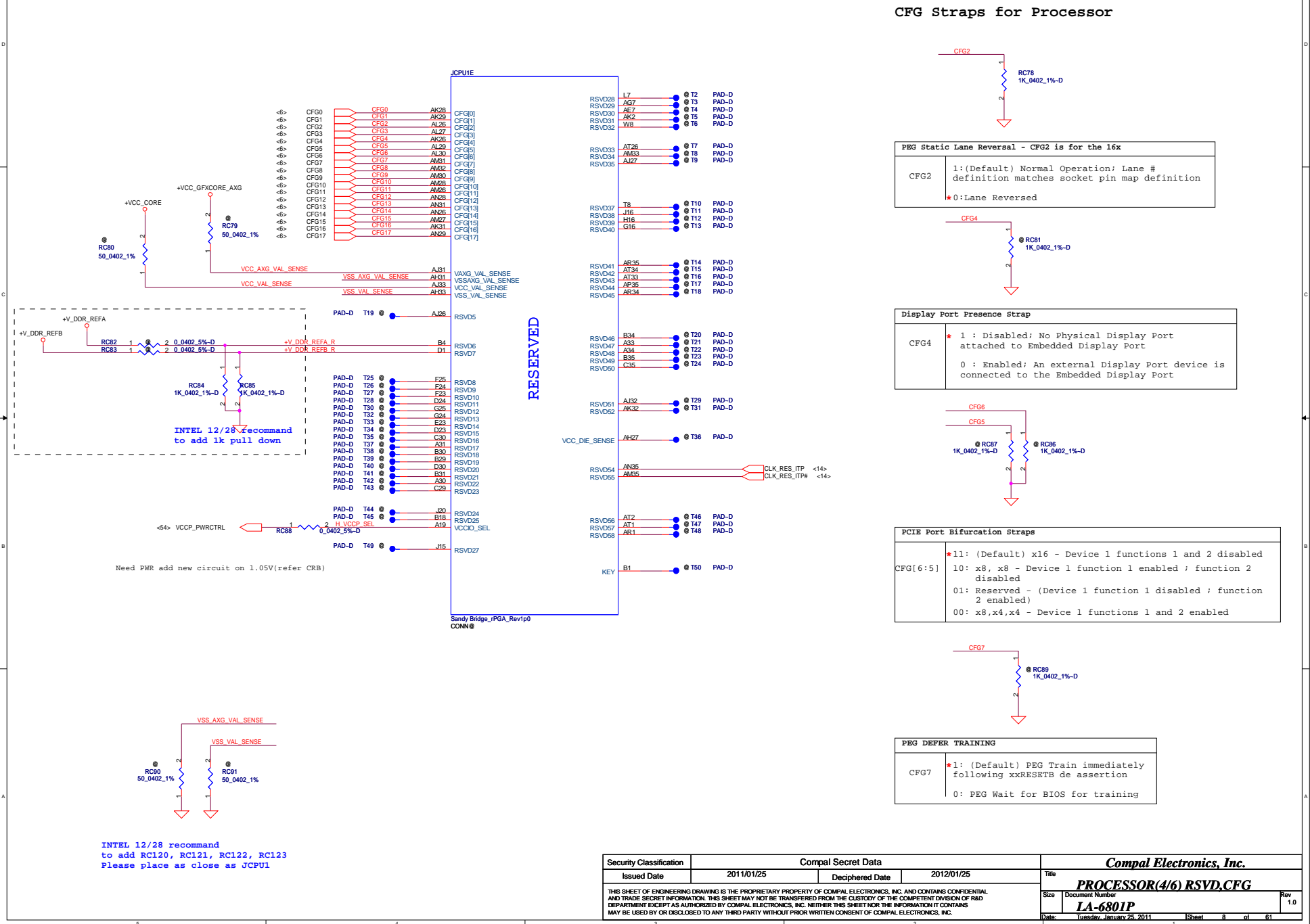
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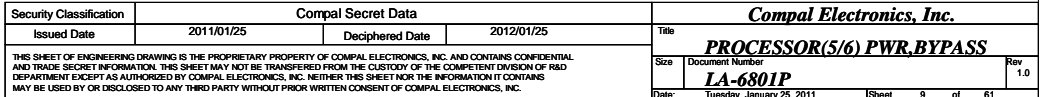


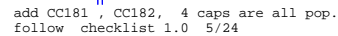
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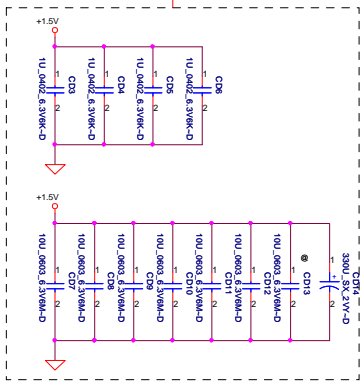


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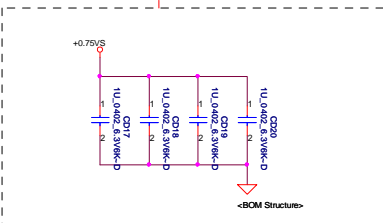
<7> DDR_A_DQS[0..7]
<7> DDR_A_DQS[0..7]
<7> DDR_A_D[0..63]
<7> DDR_A_MA[0..15]

Layout Note:
Place near JDIMM1

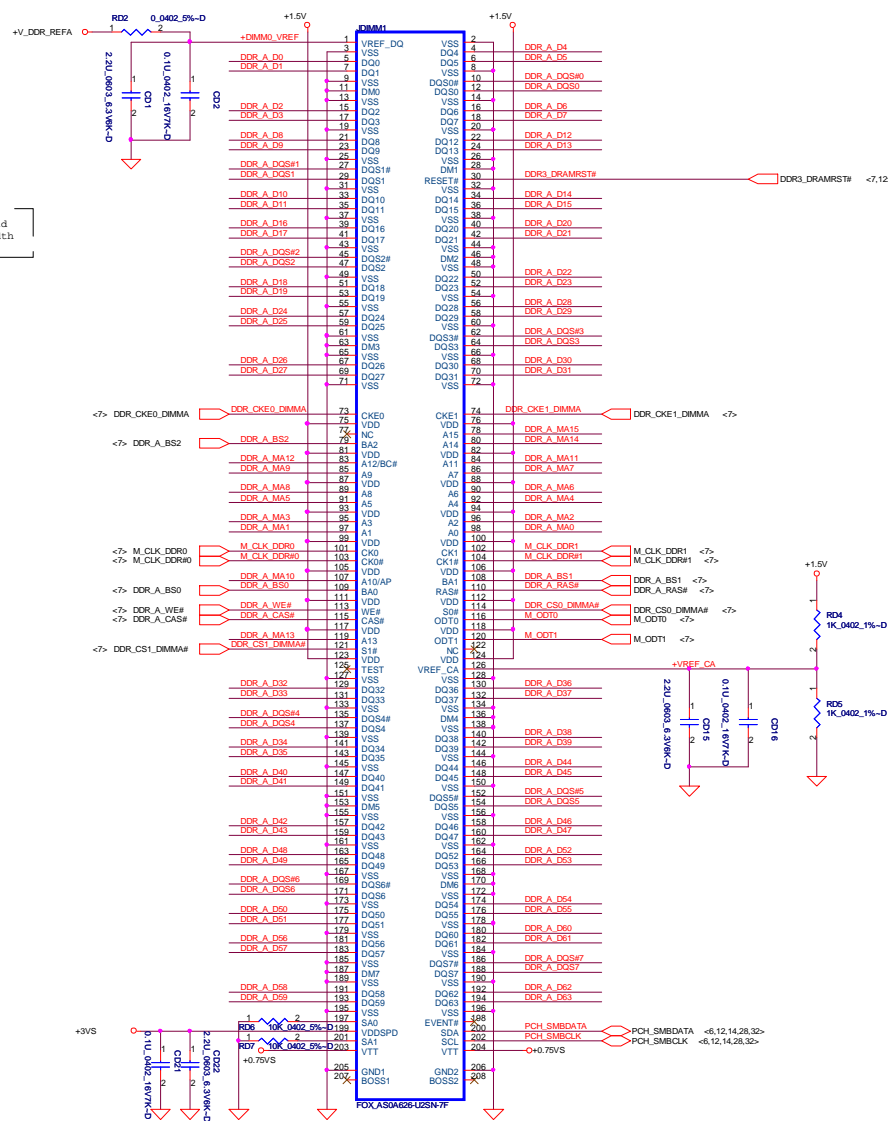
All VREF traces should
have 10 mil trace width



Layout Note:
Place near JDIMM1.203,204



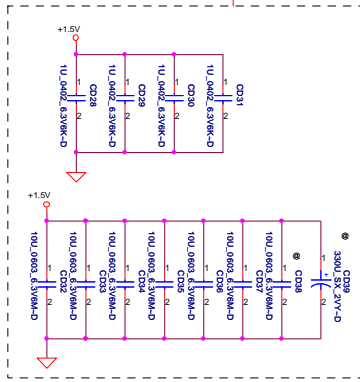
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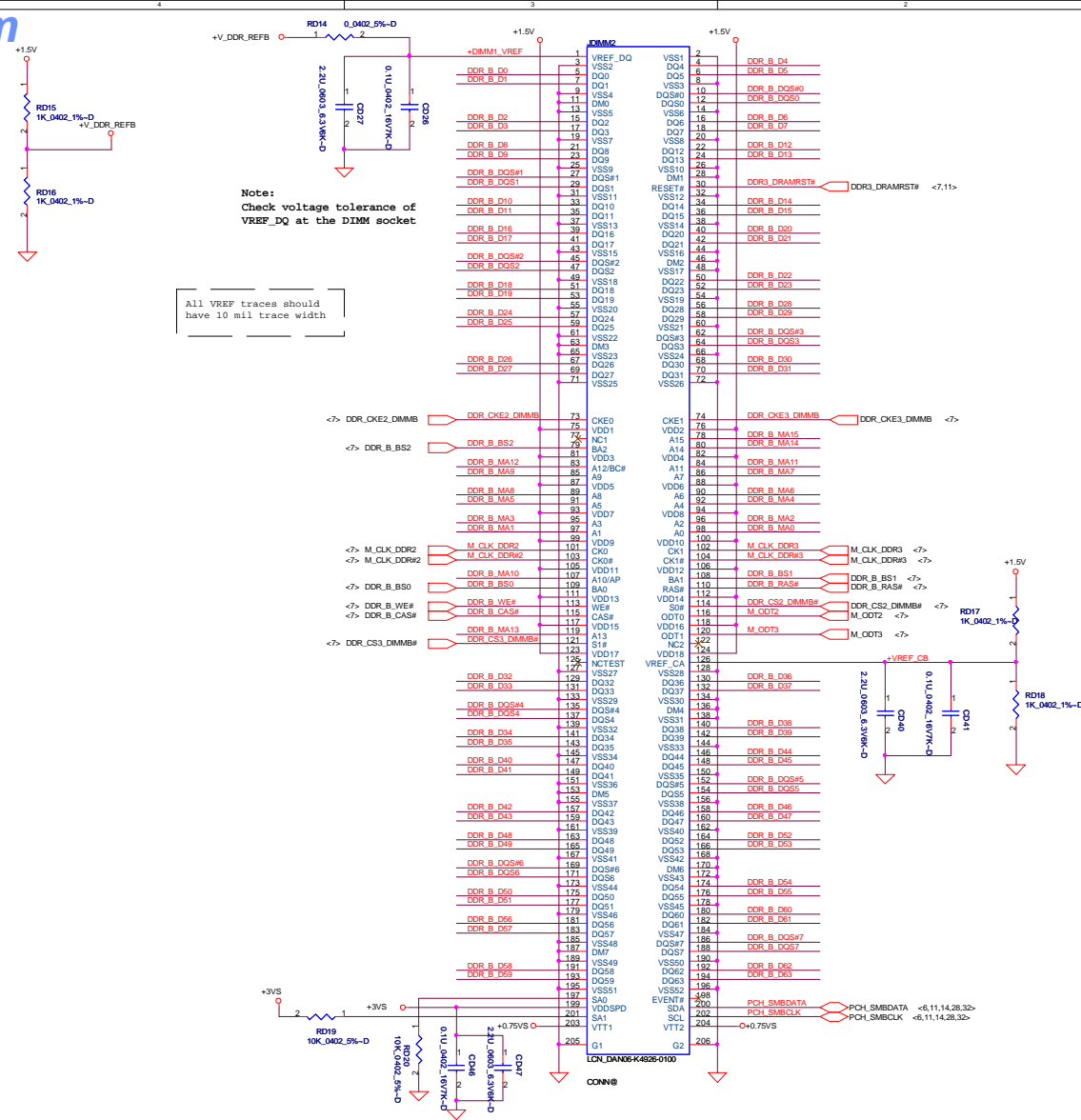
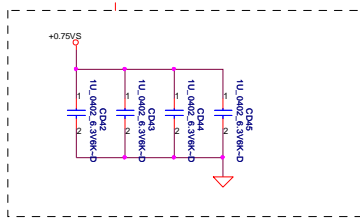
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<7> DDR_B_DQS#(0.7)
<7> DDR_B_DQS(0.7)
<7> DDR_B_DQ.63
<7> DDR_B_MA(0.15)

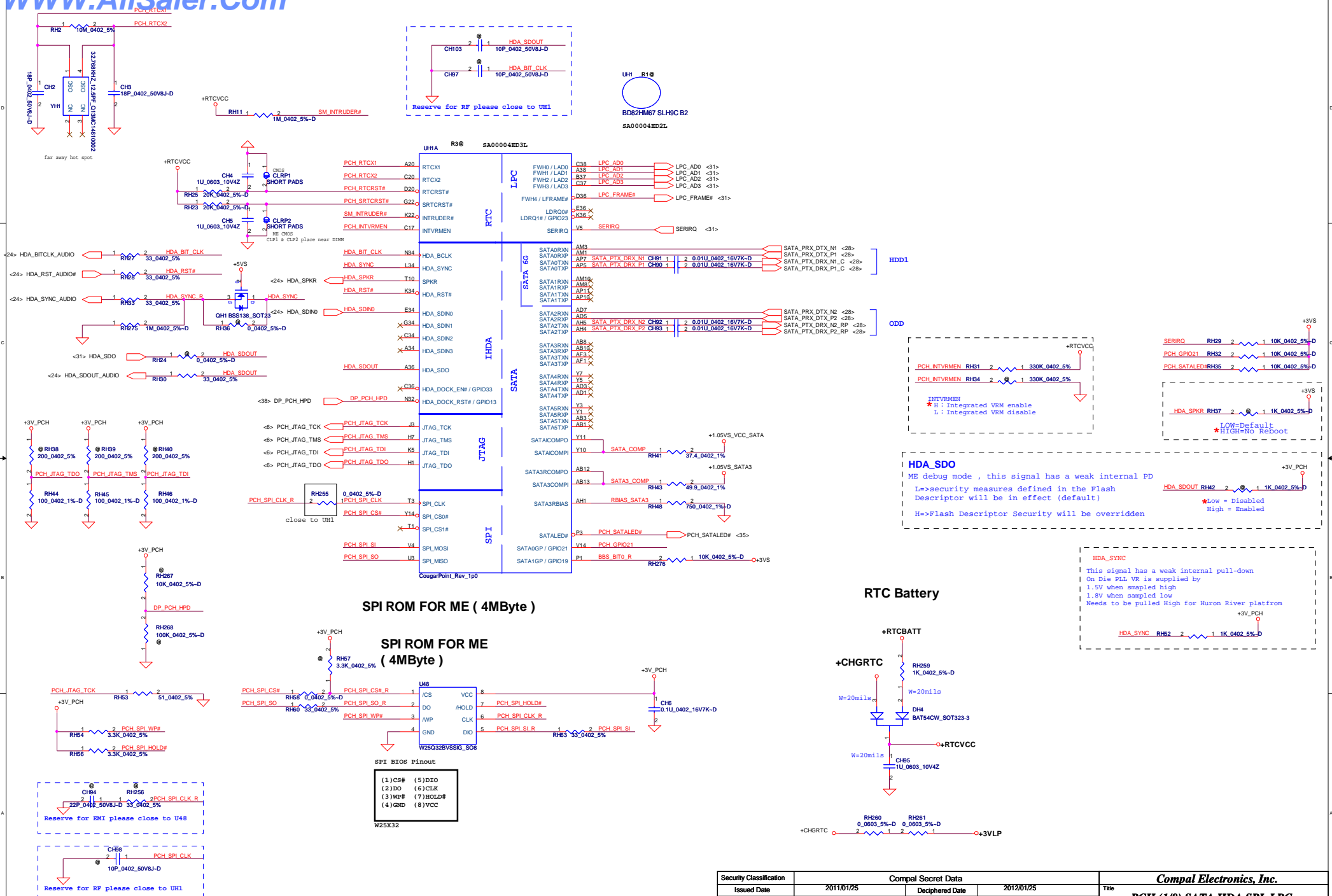
Layout Note:
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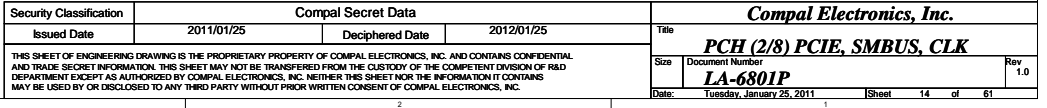
Layout Note:
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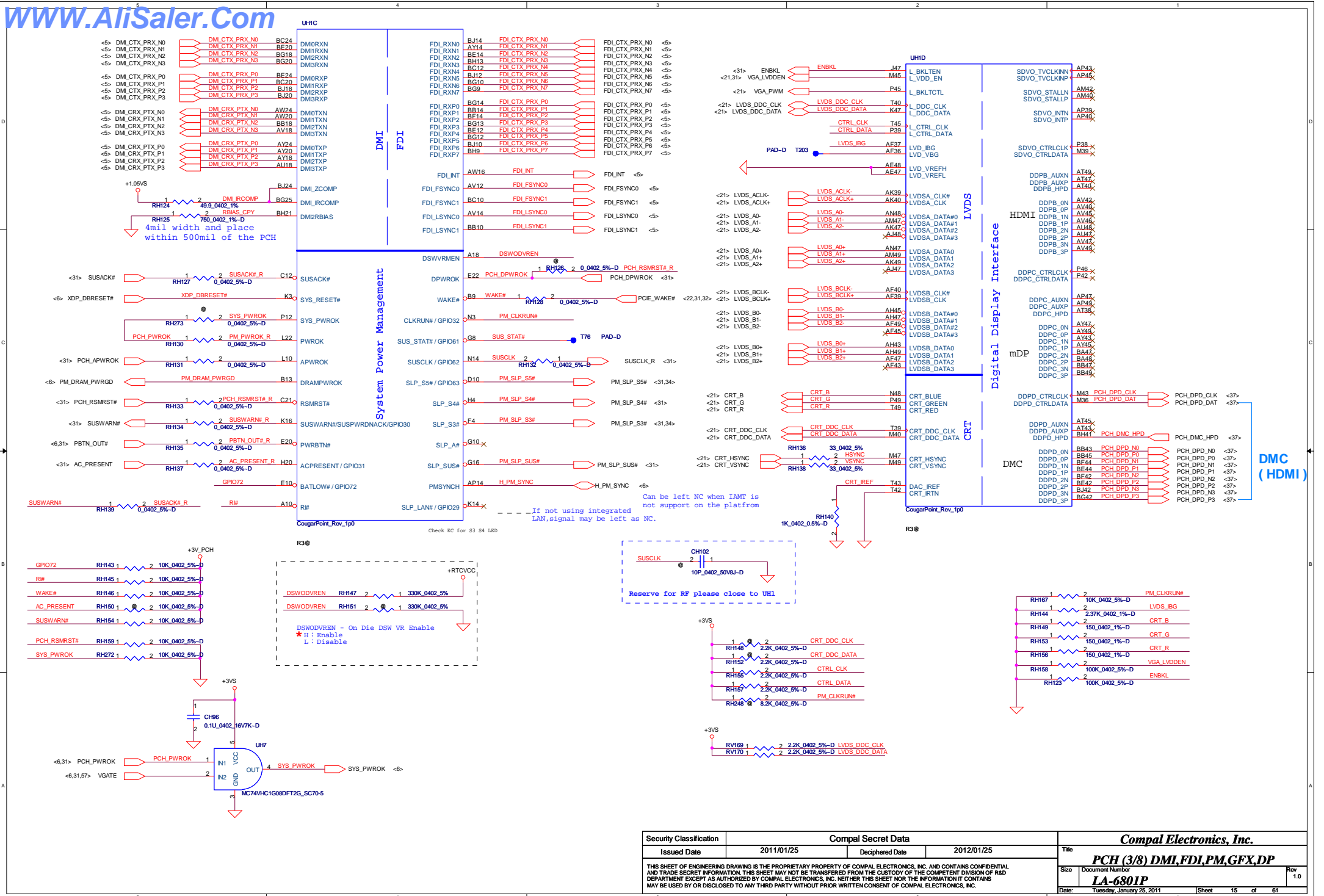


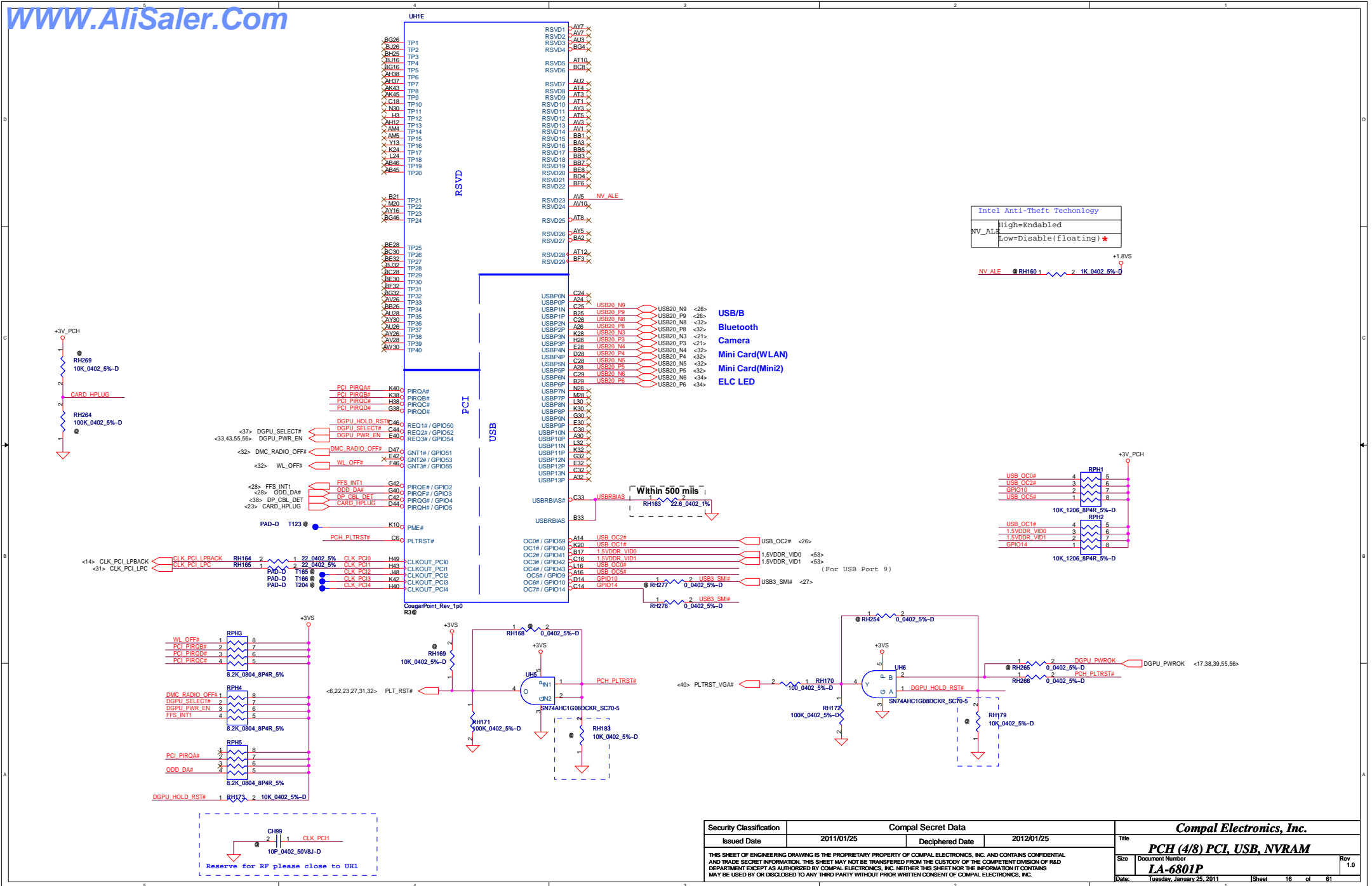
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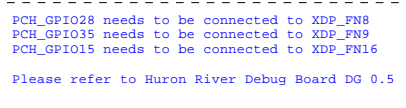
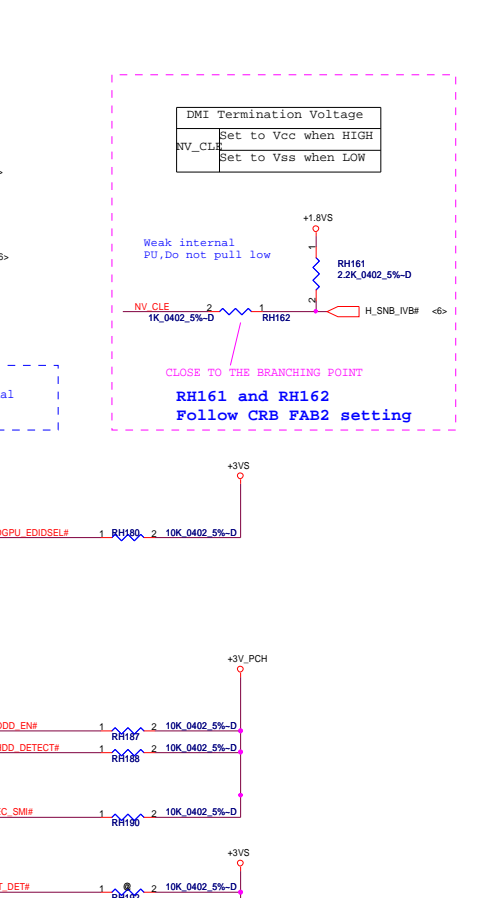
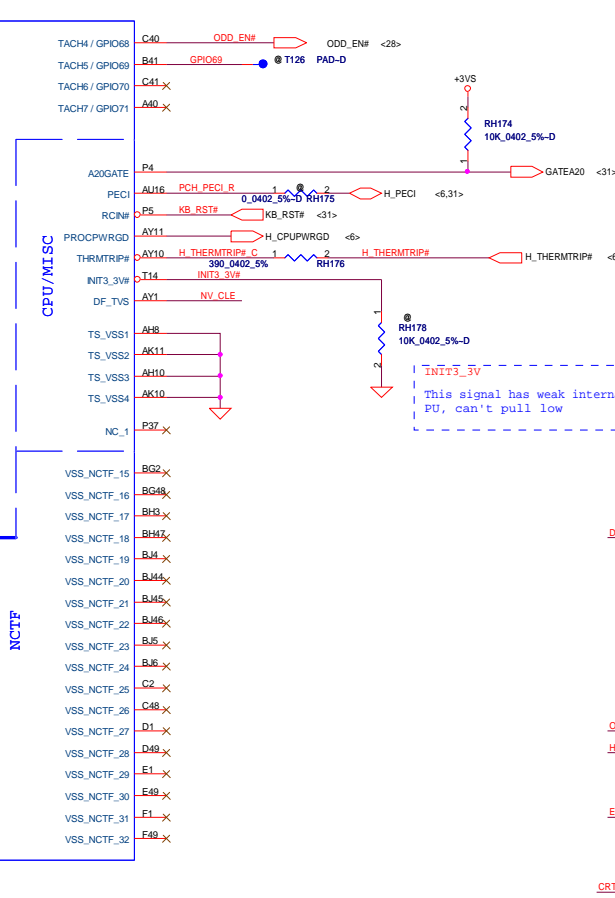
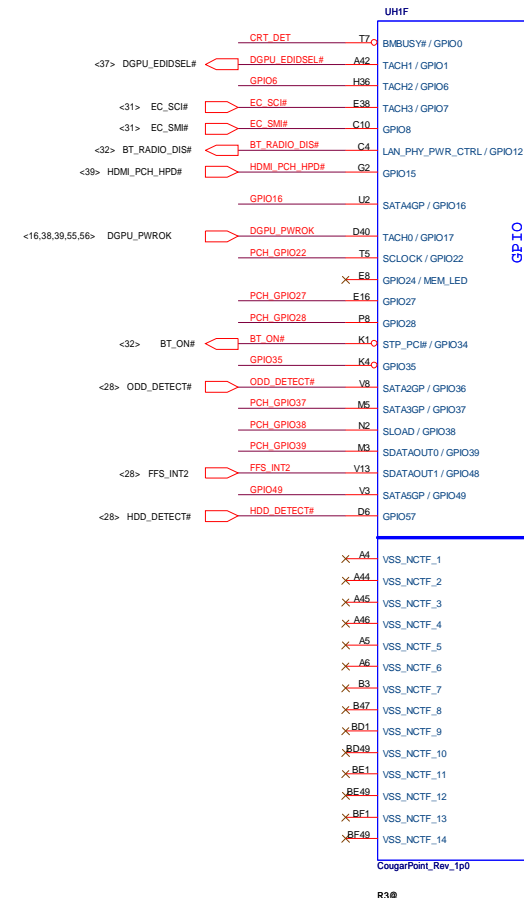
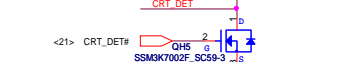
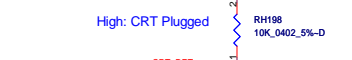
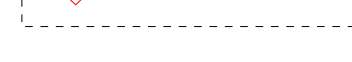
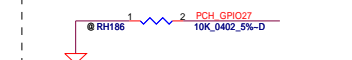
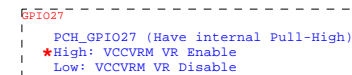
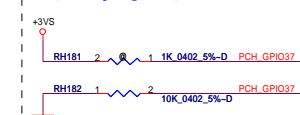
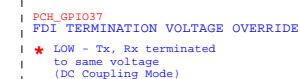
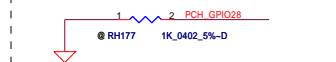
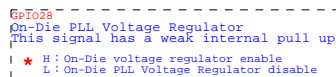
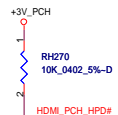


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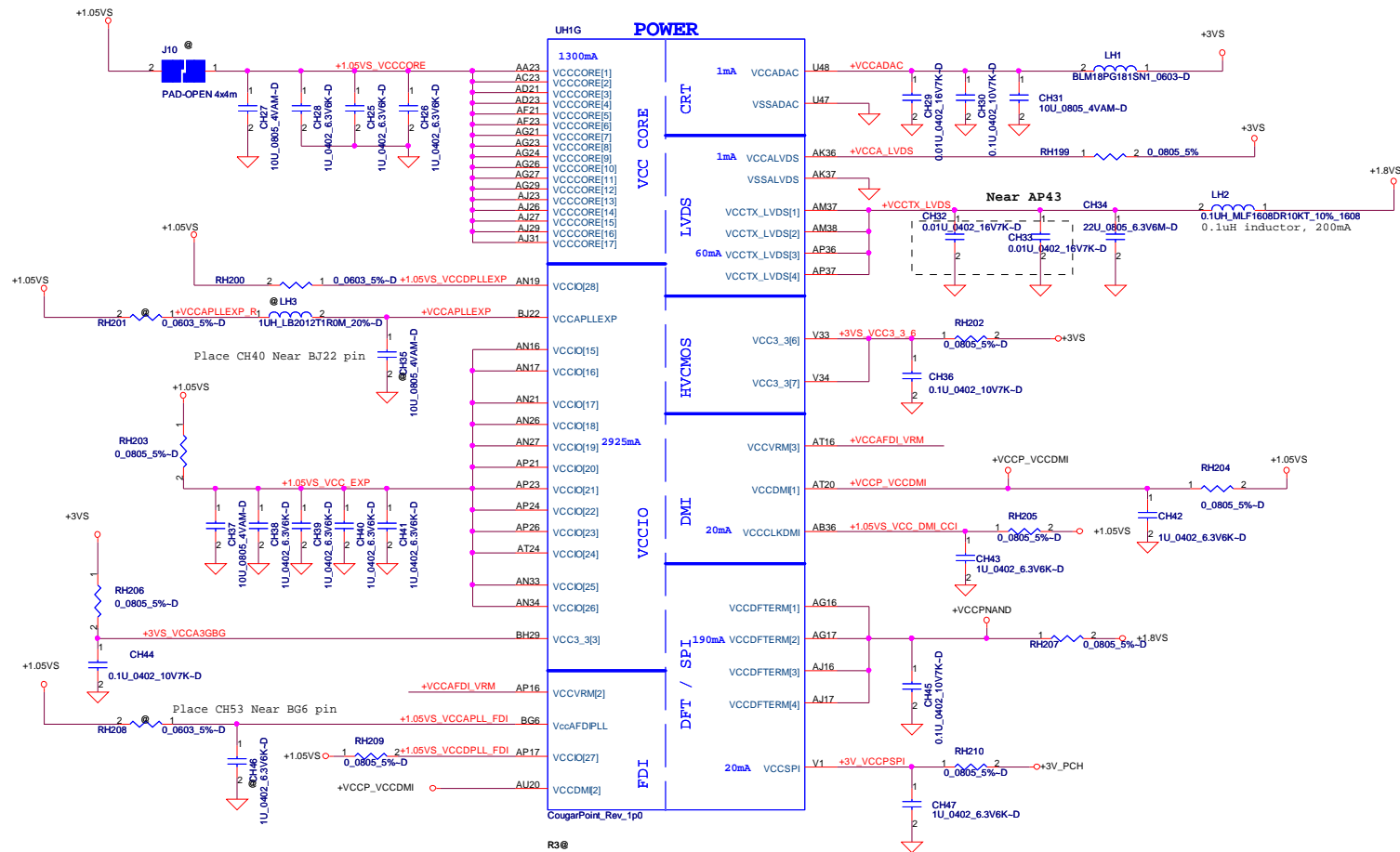






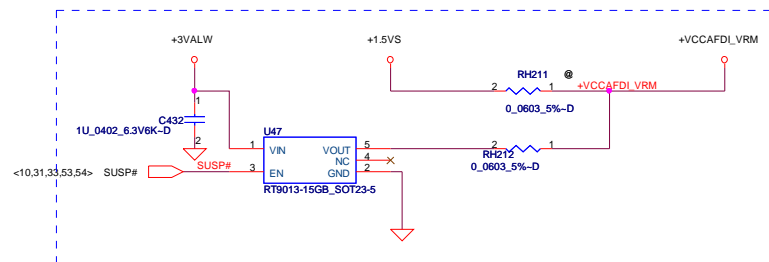


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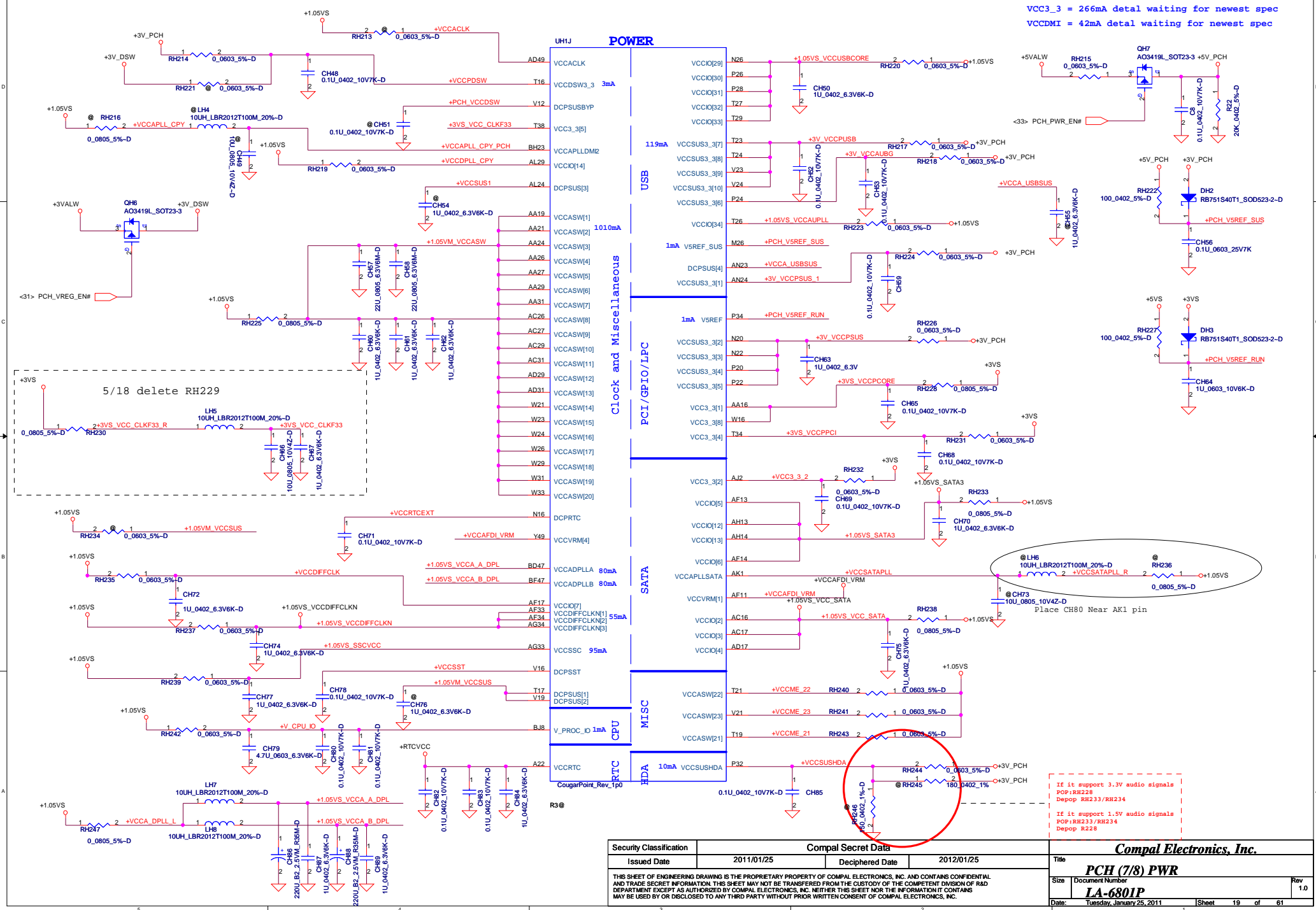


PCH Power Rail Table		
Voltage Rail	Voltage	50 Iocmax Current (A)
V_PROC_IO	1.05	0.001
V5REF	5	0.001
V5REF_Sus	5	0.001
Vcc3_3	3.3	0.266
VccADAC	3.3	0.001
VccADPLLA	1.05	0.08
VccADPLLB	1.05	0.08
VccCore	1.05	1.3
VccDMI	1.05	0.042
VccIO	1.05	2.925
VccASW	1.05	1.01
VccSPI	3.3	0.02
VccDSW	3.3	0.003
VccpNAND	1.8	0.19
VccRTC	3.3	6 uA
VccSus3_3	3.3	0.119
VccSusHDA	3.3 / 1.5	0.01
VccVRM	1.8 / 1.5	0.16
VccCLKDMI	1.05	0.02
VccSSC	1.05	0.095
VccDIFFCLKN	1.05	0.055
VccALVDS	3.3	0.001
VccTX_LVDS	1.8	0.06

VCCVRM = 160mA detal waiting for newest spec



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UH1H		
H5	VSS[0]	
AA17	VSS[1]	VSS[80] AK38
AA2	VSS[2]	VSS[81] AK4
AA3	VSS[3]	VSS[82] AK42
AA33	VSS[4]	VSS[83] AK46
AA34	VSS[5]	VSS[84] AK8
AB11	VSS[6]	VSS[85] AL16
AB14	VSS[7]	VSS[86] AL17
AB39	VSS[8]	VSS[87] AL19
AB4	VSS[9]	VSS[88] AL2
AB43	VSS[10]	VSS[89] AL21
AB5	VSS[11]	VSS[90] AL23
AB7	VSS[12]	VSS[91] AL26
AC19	VSS[13]	VSS[92] AL27
AC2	VSS[14]	VSS[93] AL31
AC21	VSS[15]	VSS[94] AL33
AC24	VSS[16]	VSS[95] AL34
AC33	VSS[17]	VSS[96] AL46
AC34	VSS[18]	VSS[97] AM11
AC48	VSS[19]	VSS[98] AM14
AD10	VSS[20]	VSS[99] AM36
AD11	VSS[21]	VSS[100] AM39
AD12	VSS[22]	VSS[101] AM43
AD13	VSS[23]	VSS[102] AM45
AD19	VSS[24]	VSS[103] AM46
AD24	VSS[25]	VSS[104] AM7
AD26	VSS[26]	VSS[105] AN2
AD27	VSS[27]	VSS[106] AN29
AD33	VSS[28]	VSS[107] AN3
AD34	VSS[29]	VSS[108] AN31
AD36	VSS[30]	VSS[109] AP12
AD37	VSS[31]	VSS[110] AP19
AD38	VSS[32]	VSS[111] AP28
AD39	VSS[33]	VSS[112] AP30
AD4	VSS[34]	VSS[113] AP32
AD40	VSS[35]	VSS[114] AP28
AD42	VSS[36]	VSS[115] AP4
AD43	VSS[37]	VSS[116] AP42
AD45	VSS[38]	VSS[117] AP46
AD46	VSS[39]	VSS[118] AP28
AD8	VSS[40]	VSS[119] AR2
AE2	VSS[41]	VSS[120] AR48
AE3	VSS[42]	VSS[121] AT11
AE10	VSS[43]	VSS[122] AT13
AE12	VSS[44]	VSS[123] AT18
AD14	VSS[45]	VSS[124] AT22
AD16	VSS[46]	VSS[125] AT26
AF16	VSS[47]	VSS[126] AT28
AF19	VSS[48]	VSS[127] AT30
AF24	VSS[49]	VSS[128] AT32
AF26	VSS[50]	VSS[129] AT34
AF27	VSS[51]	VSS[130] AT39
AF29	VSS[52]	VSS[131] AT42
AF31	VSS[53]	VSS[132] AT46
AF38	VSS[54]	VSS[133] AT7
AF4	VSS[55]	VSS[134] AU24
AF42	VSS[56]	VSS[135] AU30
AF46	VSS[57]	VSS[136] AV16
AF5	VSS[58]	VSS[137] AV20
AF7	VSS[59]	VSS[138] AV24
AF8	VSS[60]	VSS[139] AV30
AG19	VSS[61]	VSS[140] AV38
AG2	VSS[62]	VSS[141] AV4
AG31	VSS[63]	VSS[142] AV43
AG48	VSS[64]	VSS[143] AV8
AH11	VSS[65]	VSS[144] AW14
AH3	VSS[66]	VSS[145] AW18
AH36	VSS[67]	VSS[146] AW2
AH39	VSS[68]	VSS[147] AW22
AH40	VSS[69]	VSS[148] AW26
AH42	VSS[70]	VSS[149] AW28
AH46	VSS[71]	VSS[150] AW32
AH7	VSS[72]	VSS[151] AW34
AJ19	VSS[73]	VSS[152] AW36
AJ21	VSS[74]	VSS[153] AW40
AJ24	VSS[75]	VSS[154] AW48
AJ33	VSS[76]	VSS[155] AV11
AJ34	VSS[77]	VSS[156] AY12
AK12	VSS[78]	VSS[157] AY22
AK3	VSS[79]	VSS[158] AY28

CougarPoint_Rev_1p0

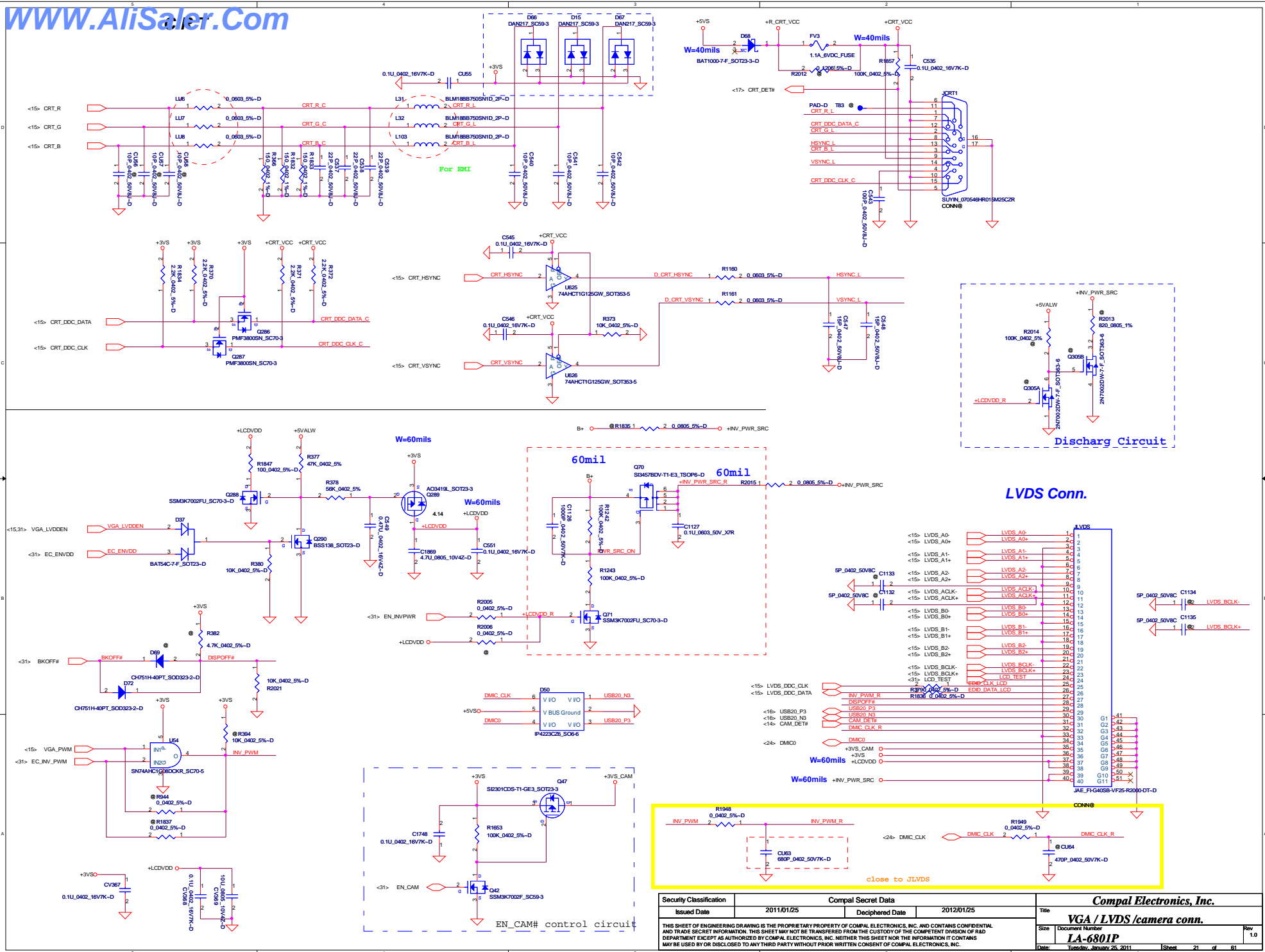
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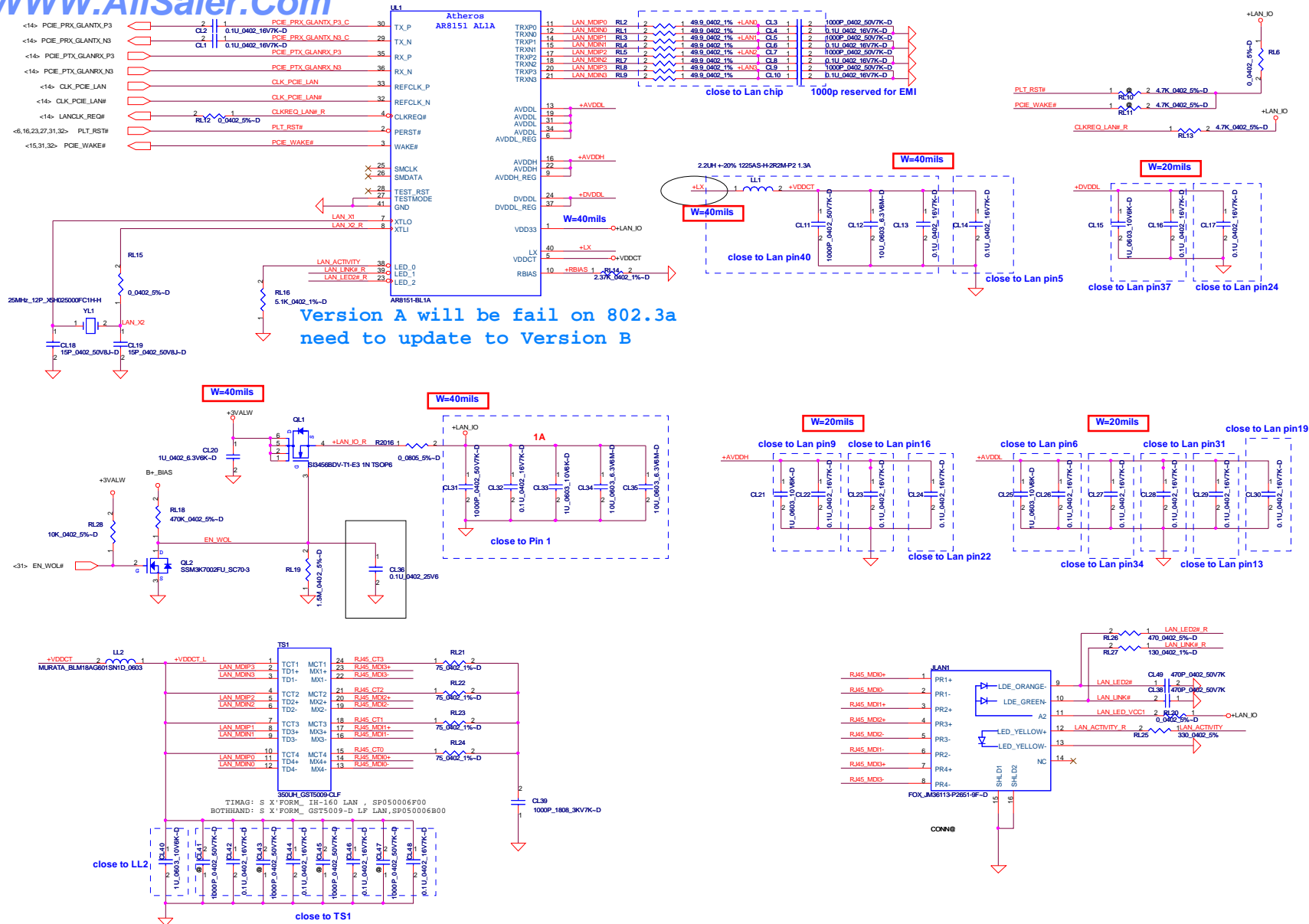
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AY8	VSS[162]	VSS[262] K39
B11	VSS[163]	VSS[263] K46
B19	VSS[164]	VSS[264] L18
B23	VSS[165]	VSS[265] L2
B27	VSS[166]	VSS[266] L20
B31	VSS[167]	VSS[267] L26
B35	VSS[168]	VSS[268] L28
B39	VSS[169]	VSS[269] L36
B7	VSS[170]	VSS[270] L48
F45	VSS[171]	VSS[271] M12
BB12	VSS[172]	VSS[272] P16
BB16	VSS[173]	VSS[273] M18
BB20	VSS[174]	VSS[274] M22
BB22	VSS[175]	VSS[275] M24
BB24	VSS[176]	VSS[276] M30
BB28	VSS[177]	VSS[277] M32
BB30	VSS[178]	VSS[278] M34
BB38	VSS[179]	VSS[279] M38
BB4	VSS[180]	VSS[280] M4
BB46	VSS[181]	VSS[281] M42
BC14	VSS[182]	VSS[282] M46
BC18	VSS[183]	VSS[283] M48
BC2	VSS[184]	VSS[284] M8
BC22	VSS[185]	VSS[285] N18
BC26	VSS[186]	VSS[286] P30
BC32	VSS[187]	VSS[287] N47
BC36	VSS[188]	VSS[288] P11
BC38	VSS[189]	VSS[289] P18
BC40	VSS[190]	VSS[290] T33
BC42	VSS[191]	VSS[291] P40
BC44	VSS[192]	VSS[292] P43
BD46	VSS[193]	VSS[293] P47
BD5	VSS[194]	VSS[294] P7
BD55	VSS[195]	VSS[295] R2
BE22	VSS[196]	VSS[296] R48
BE26	VSS[197]	VSS[297] T12
BE40	VSS[198]	VSS[298] T31
BE10	VSS[199]	VSS[299] T37
BE12	VSS[200]	VSS[300] T4
BF16	VSS[201]	VSS[301] W34
BF20	VSS[202]	VSS[302] T46
BF22	VSS[203]	VSS[303] T47
BF24	VSS[204]	VSS[304] T8
BF26	VSS[205]	VSS[305] V11
BF28	VSS[206]	VSS[306] V17
BD3	VSS[207]	VSS[307] V26
BF30	VSS[208]	VSS[308] V27
BF38	VSS[209]	VSS[309] V29
BF40	VSS[210]	VSS[310] V31
BG17	VSS[211]	VSS[311] V36
BG21	VSS[212]	VSS[312] V39
BG33	VSS[213]	VSS[313] V43
BG34	VSS[214]	VSS[314] V7
BG8	VSS[215]	VSS[315] W17
BH11	VSS[216]	VSS[316] W19
BH12	VSS[217]	VSS[317] W2
BH15	VSS[218]	VSS[318] W27
BH19	VSS[219]	VSS[319] W48
H10	VSS[220]	VSS[320] Y12
BH27	VSS[221]	VSS[321] Y38
BH31	VSS[222]	VSS[322] Y4
BH33	VSS[223]	VSS[323] Y42
BH35	VSS[224]	VSS[324] Y46
BH38	VSS[225]	VSS[325] Y6
BH43	VSS[226]	VSS[326] BG29
BH7	VSS[227]	VSS[327] N24
D3	VSS[228]	VSS[328] AJ3
D12	VSS[229]	VSS[329] AD47
D16	VSS[230]	VSS[330] B43
D18	VSS[231]	VSS[331] BE10
D22	VSS[232]	VSS[332] BG41
D24	VSS[233]	VSS[333] G14
D26	VSS[234]	VSS[334] H16
D28	VSS[235]	VSS[335] H18
D30	VSS[236]	VSS[336] T36
D32	VSS[237]	VSS[337] BG22
D34	VSS[238]	VSS[338] BG24
D38	VSS[239]	VSS[339] C22
D42	VSS[240]	VSS[340] AP13
D8	VSS[241]	VSS[341] M14
E18	VSS[242]	VSS[342] AP3
E26	VSS[243]	VSS[343] AP1
G18	VSS[244]	VSS[344] BE16
G20	VSS[245]	VSS[345] BC16
G26	VSS[246]	VSS[346] BG28
G28	VSS[247]	VSS[347] BJ28
G36	VSS[248]	VSS[348]
G48	VSS[249]	VSS[349]
H12	VSS[250]	VSS[350]
H18	VSS[251]	VSS[351]
H22	VSS[252]	VSS[352]
H24	VSS[253]	VSS[353]
H26	VSS[254]	VSS[354]
H30	VSS[255]	VSS[355]
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H34	VSS[257]	VSS[357]
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CougarPoint_Rev_1p0

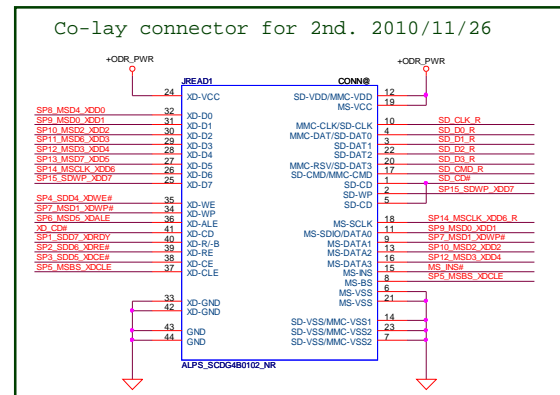
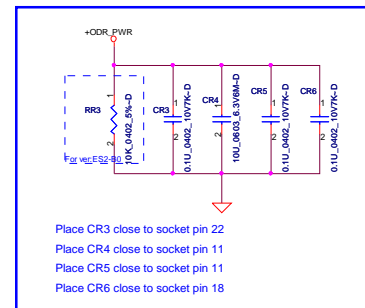
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				Sheet	20 of 61
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				IA-6801P	

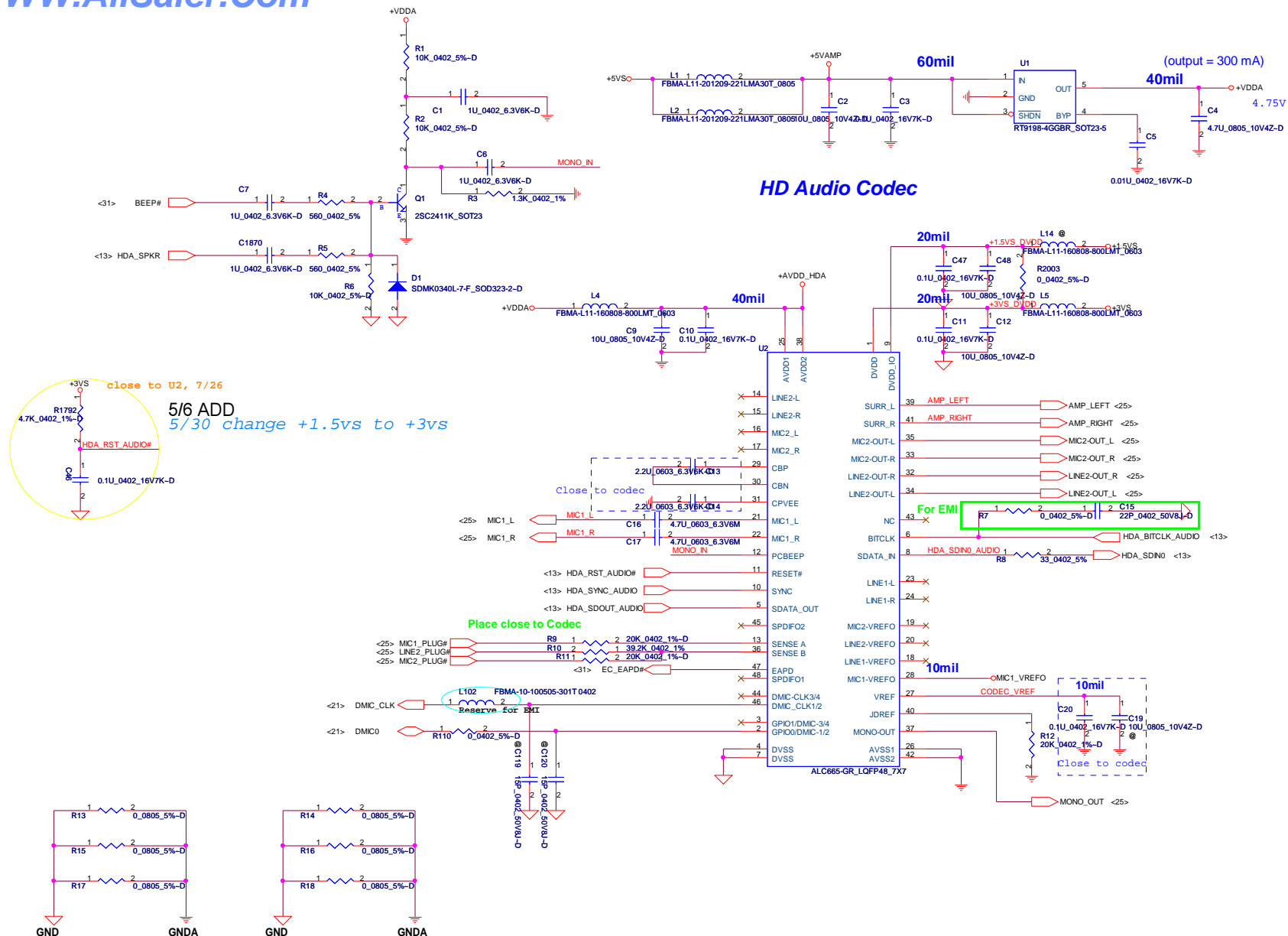




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Date:				Tuesday, January 25, 2011	Sheet	22 of 61

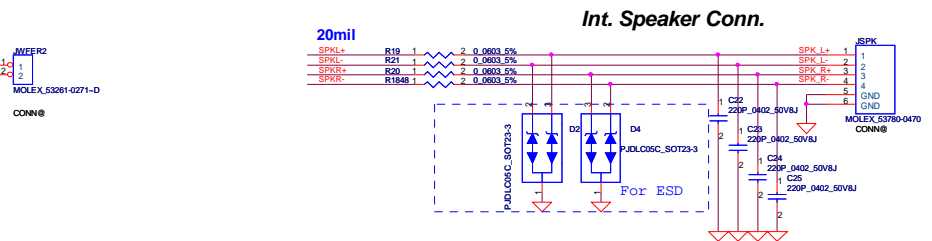
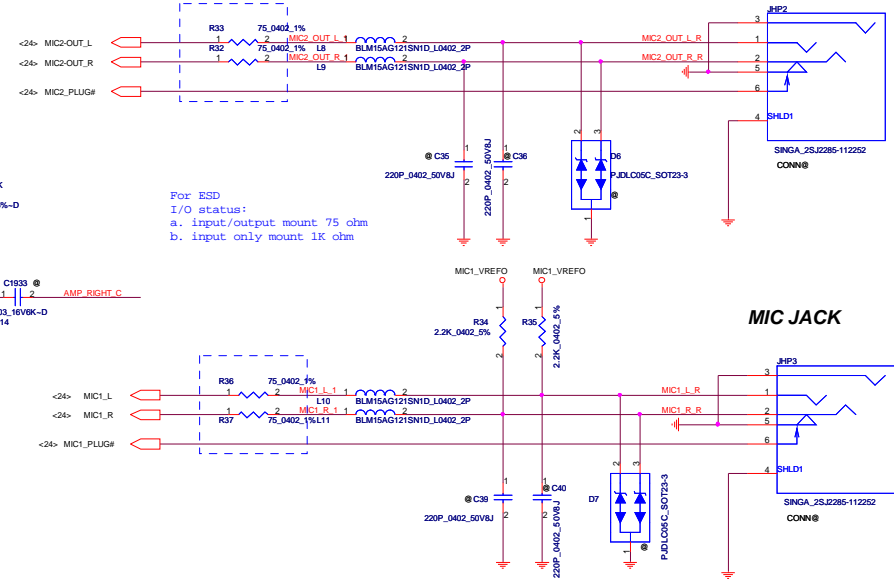
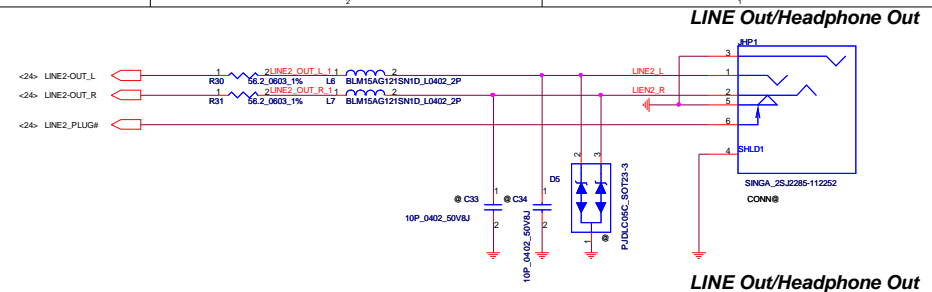
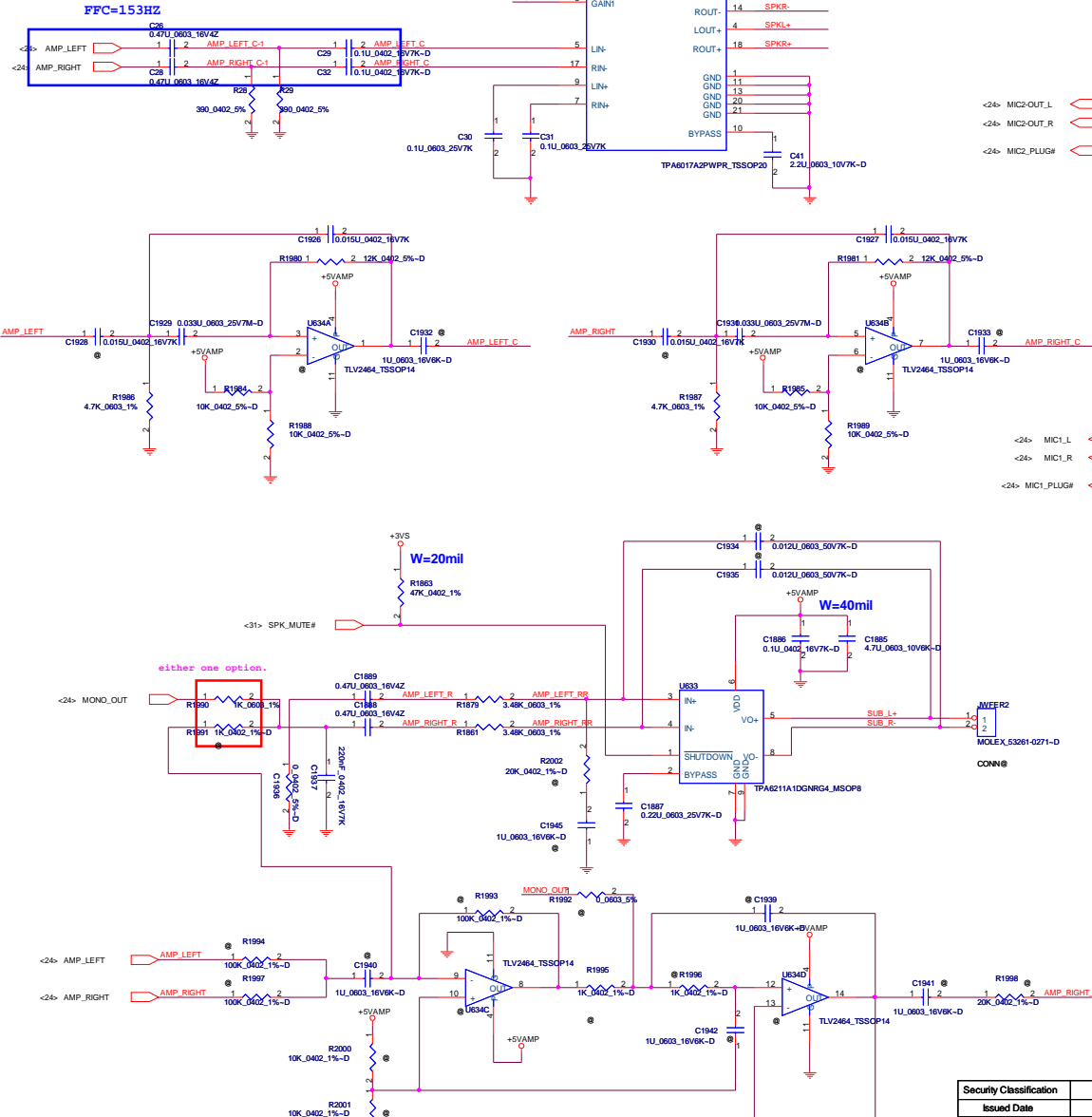
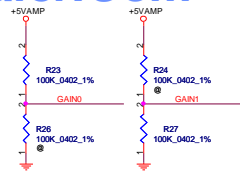


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				Date:	Rev
				Tuesday, January 25, 2011	1.0
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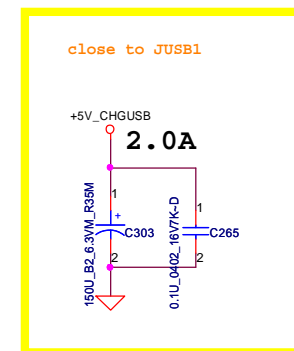
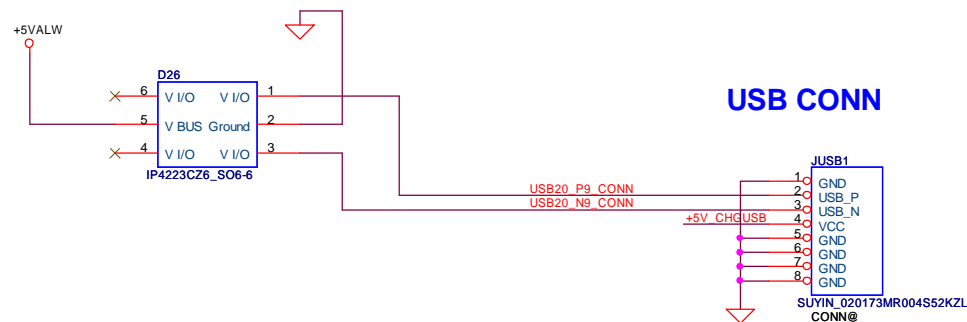
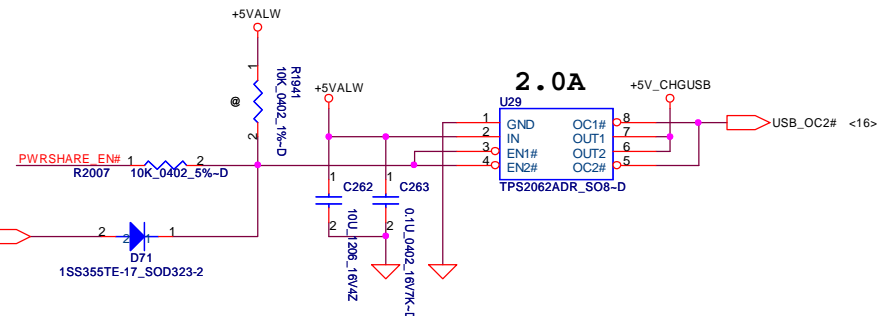
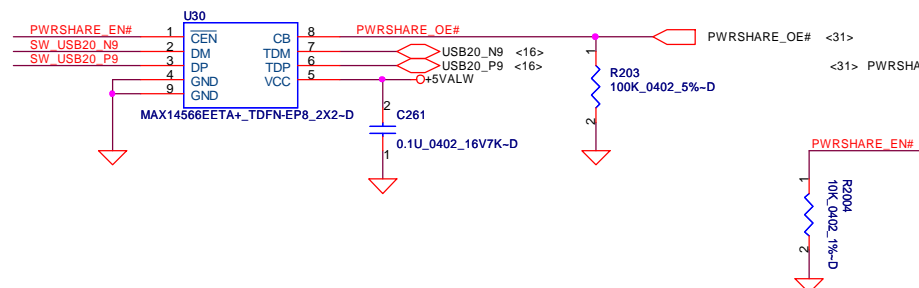
GAIN0	GAIN1	
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB



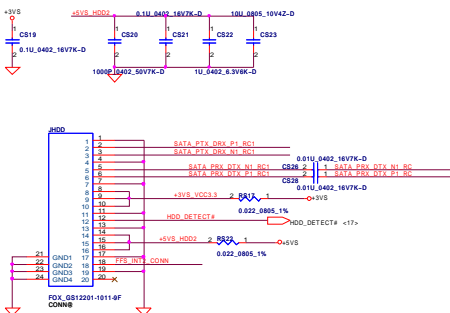
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Issued Date	2011/01/25	Deciphered Date	2012/01/25	
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				Amp TPA6017/subwoofer/ Audio Jack
				LA-6801P
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Power share

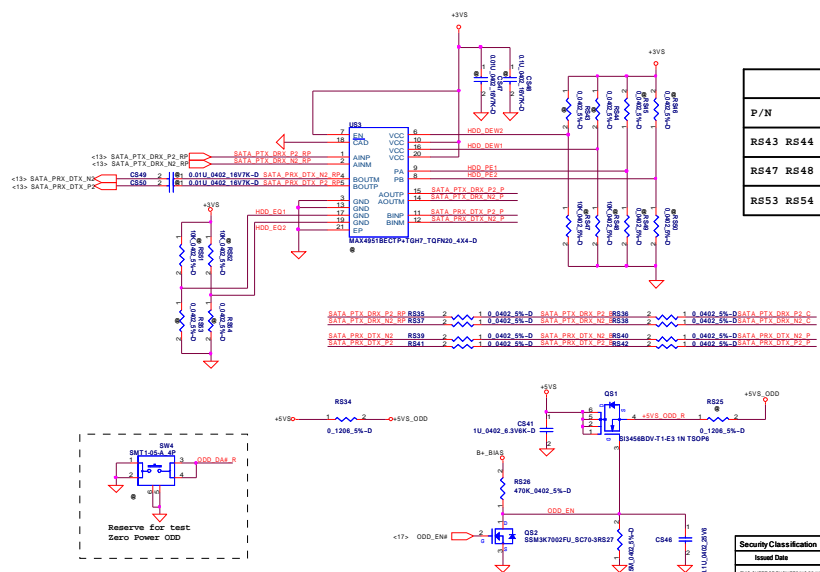
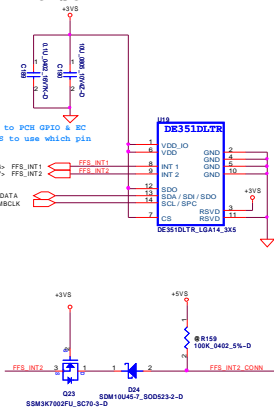
CB	Function
L	auto detection charger identification active
H	DP/DM=TDP/TDM



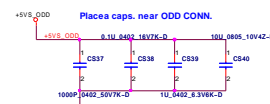
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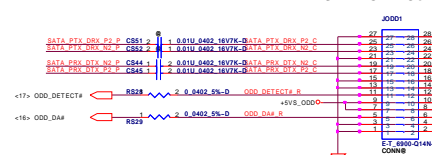
Free Fall Sensor



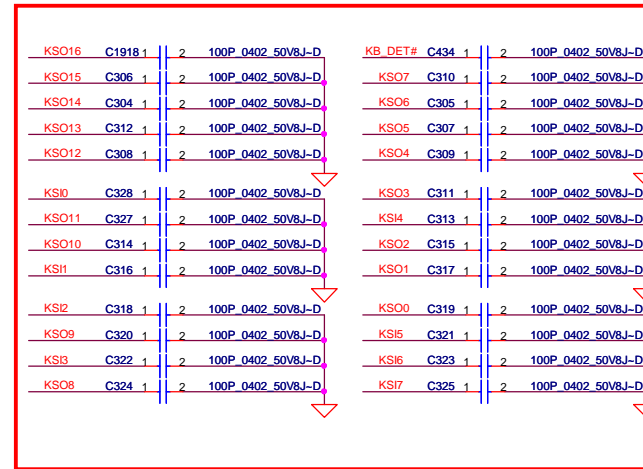
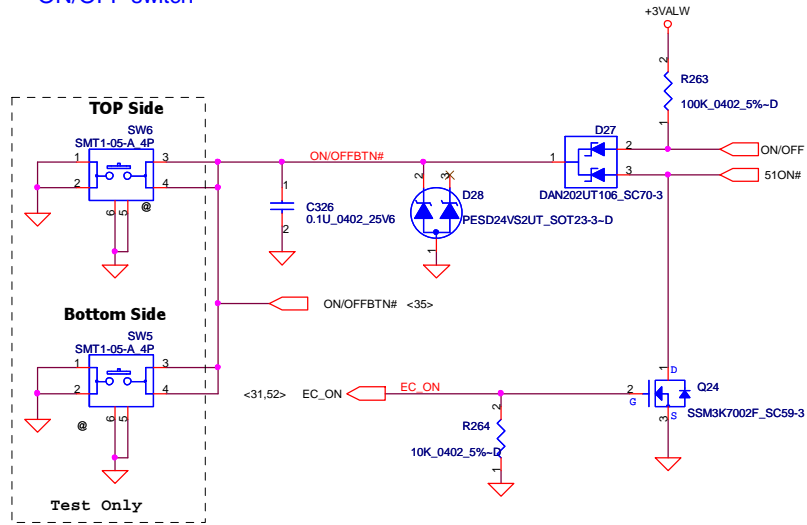
	MAXIM _{main}	TI _{2nd}
P/N	SA00003LH1L	SA00003ZX0L
RS43 RS44	pop	depop
RS47 RS48	depop	pop
RS53 RS54	pop	depop



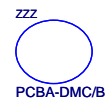
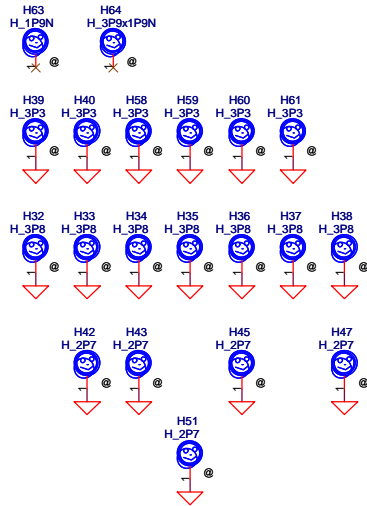
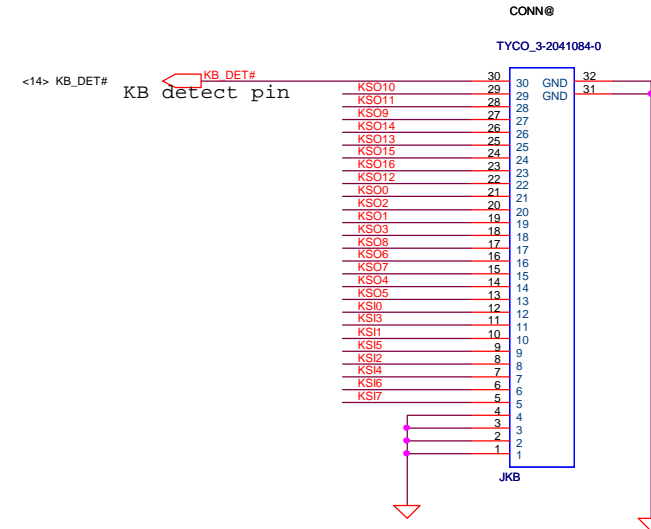
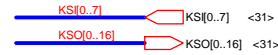
SATA ODD Conn.



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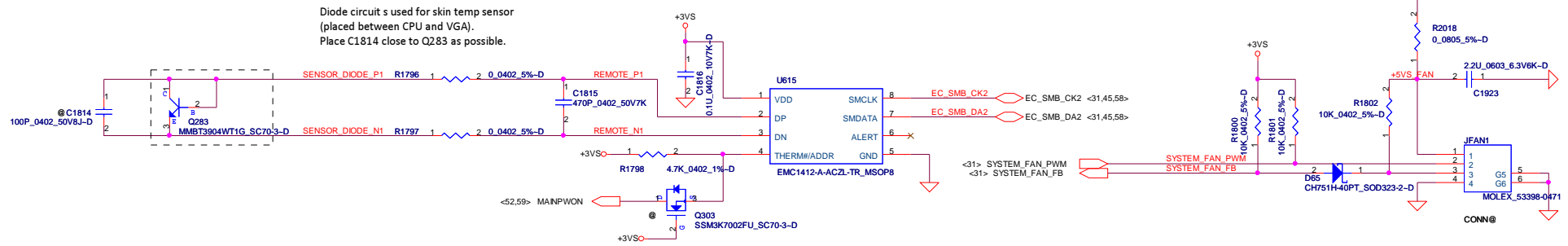
INT_KBD Conn.



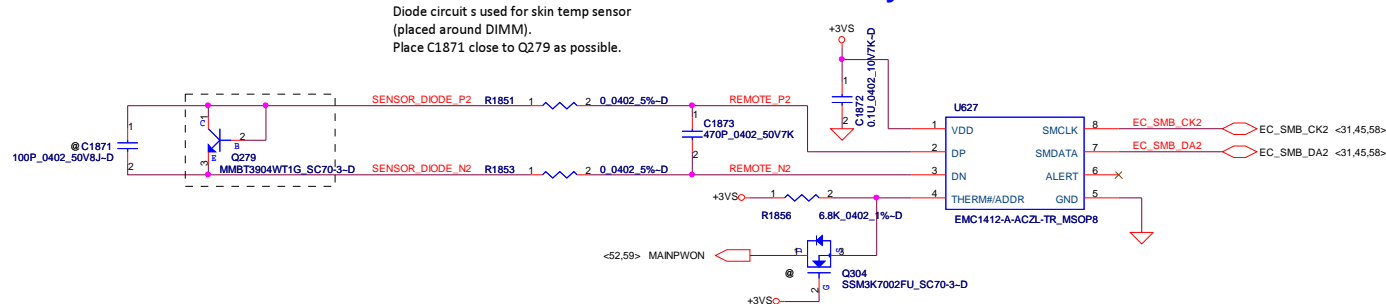
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				Date	Rev
				Tuesday, January 25, 2011	1.0
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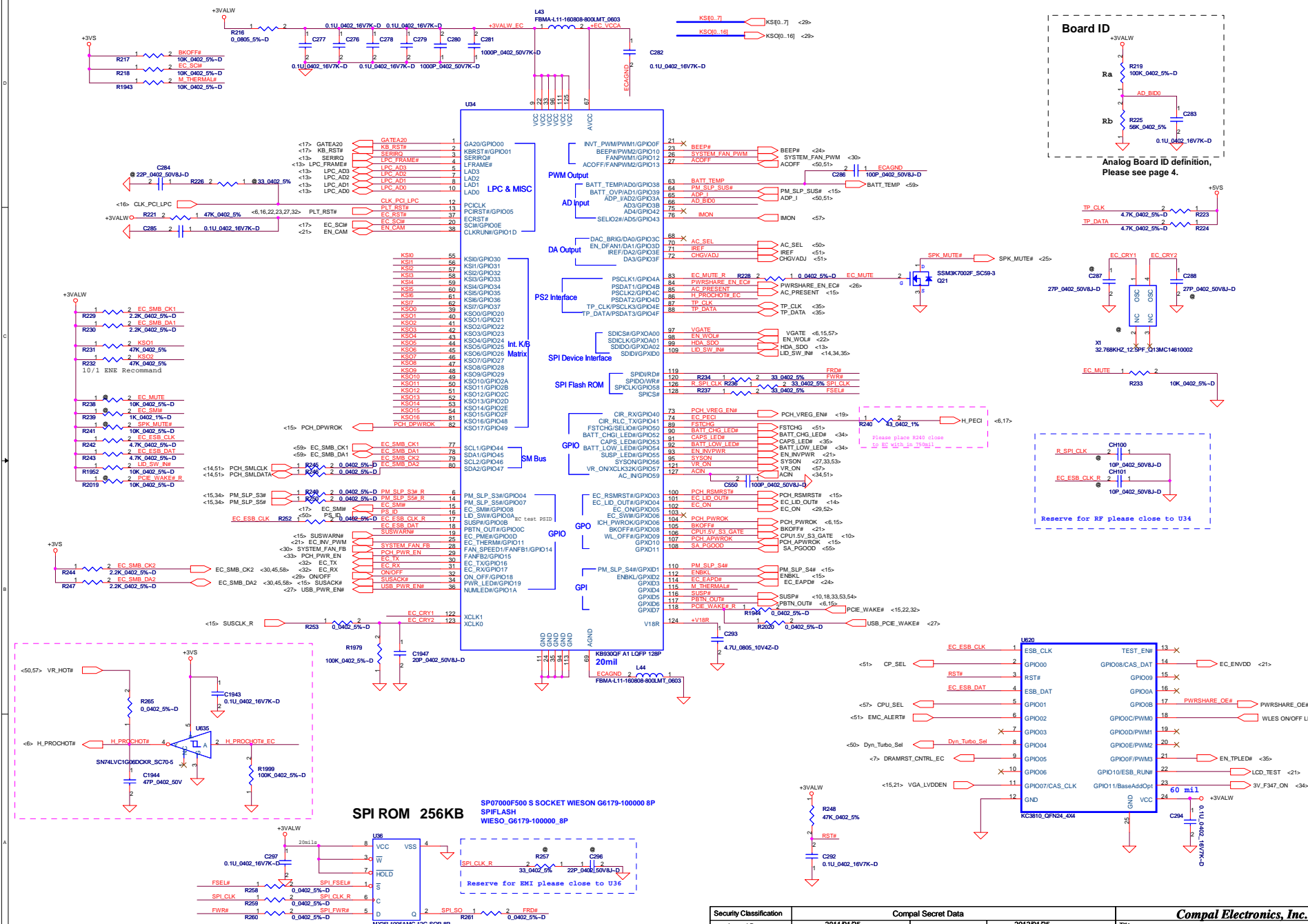
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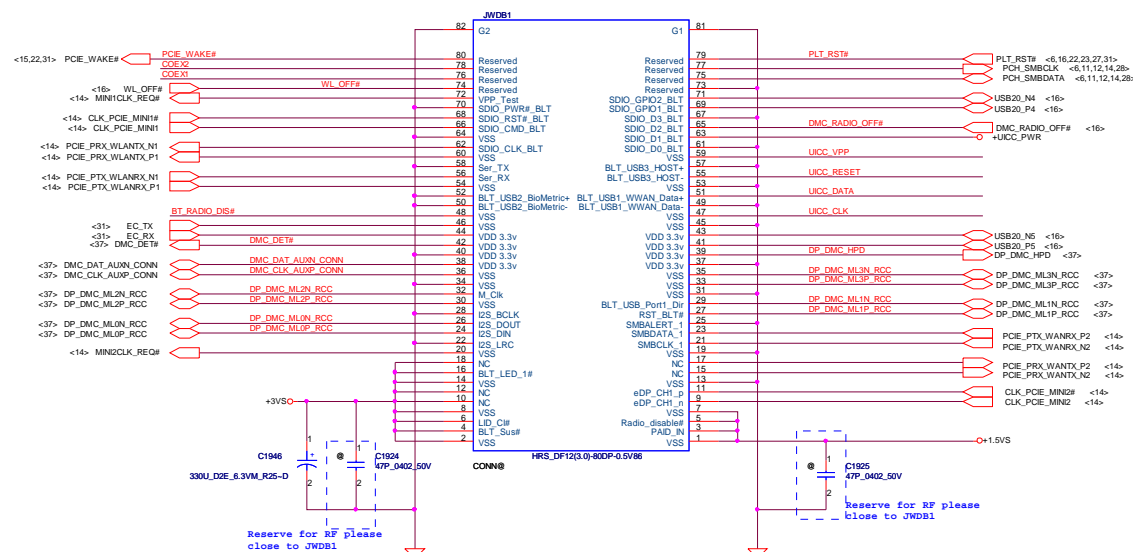
System Thermal Sensor 2



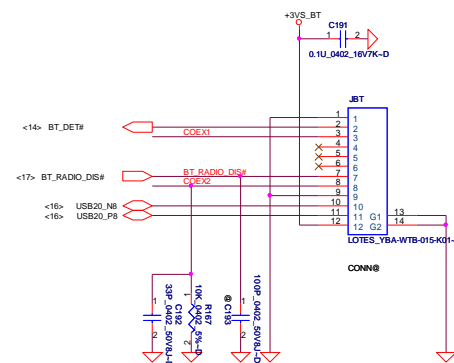
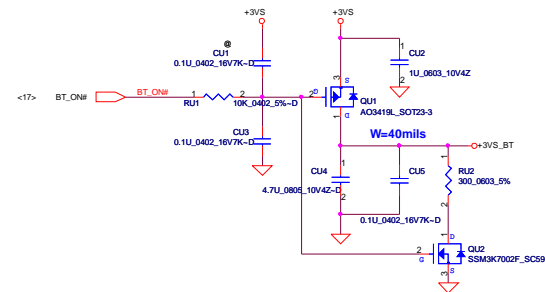
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To DMC PCB connector

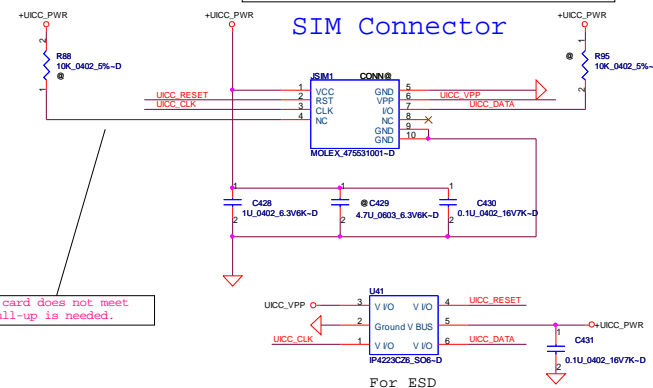


BlueTooth



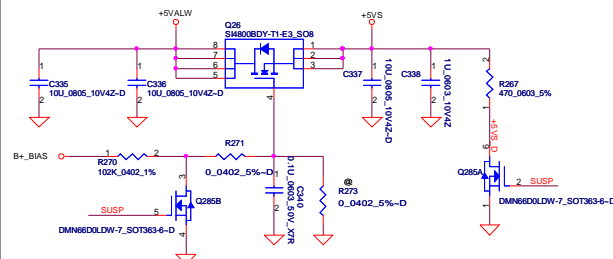
SIM card board 4.7uF change to 1uF for Tiger detect issue.

SIM Connector

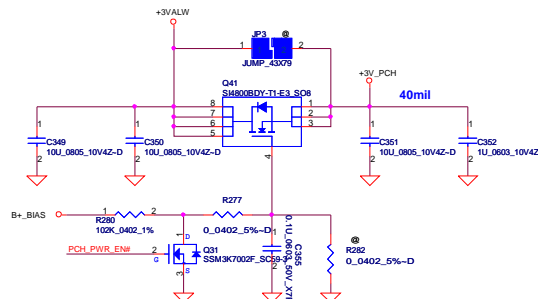
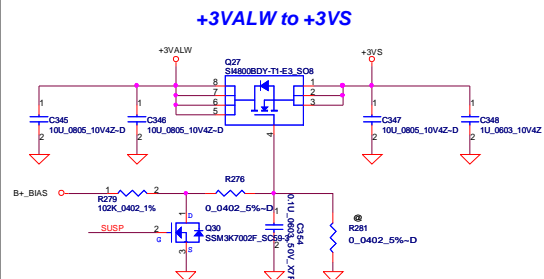


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				Tuesday, January 25, 2011	32 of 61

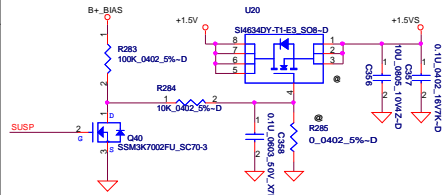
+5VALW to +5VS



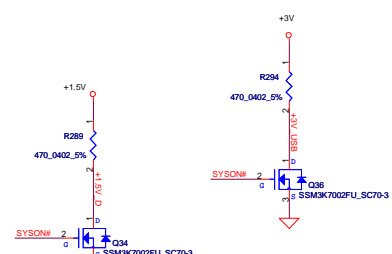
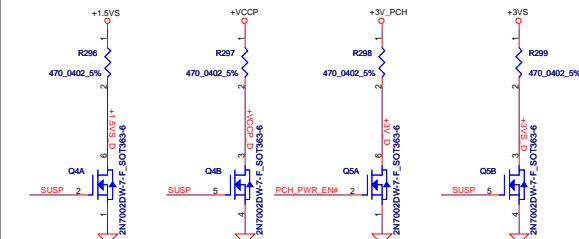
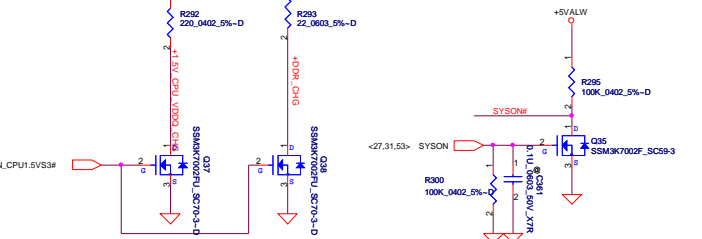
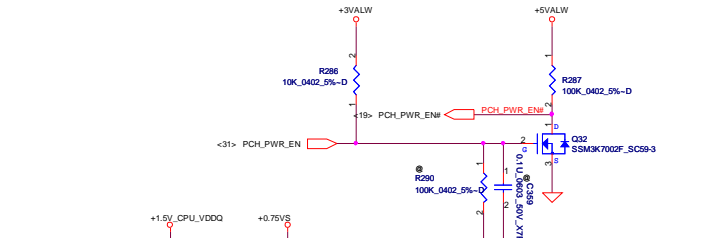
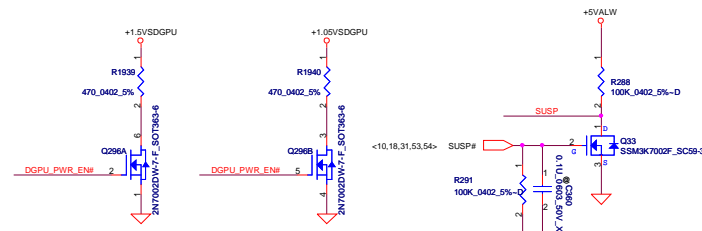
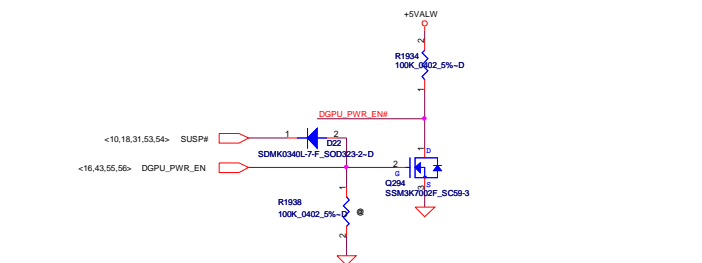
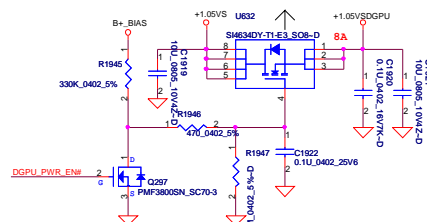
+3VALW to +3V_PCH



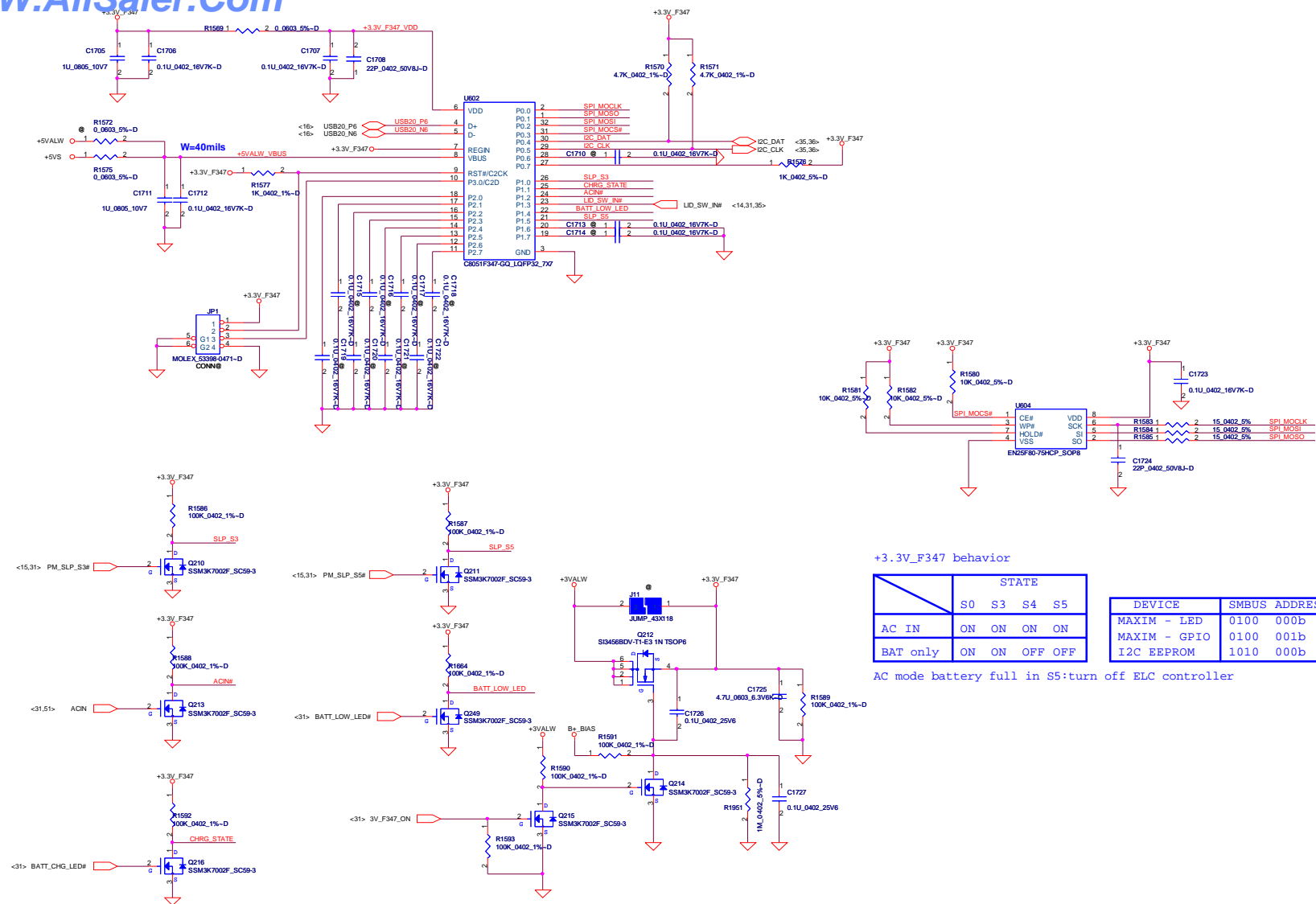
+1.5V To +1.5VS



+1.05V to +1.05VSDGPU Transfer

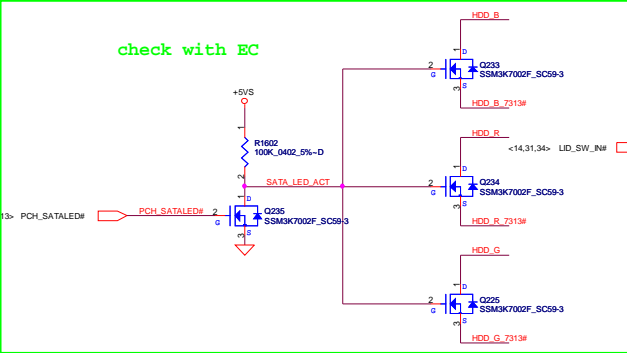
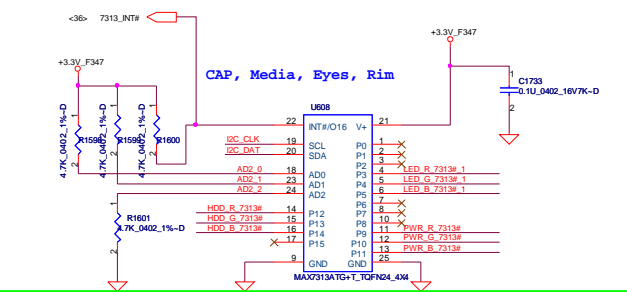
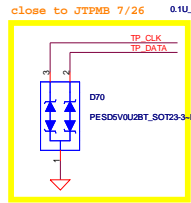
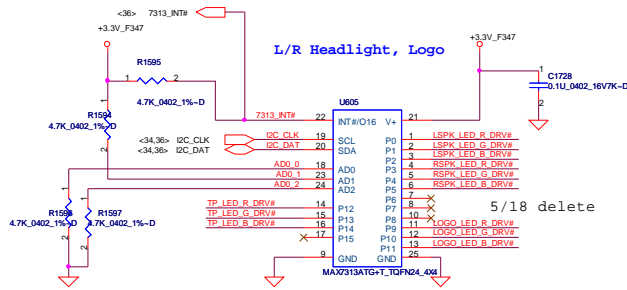


Security Classification		Compal Secret Data		Title	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	DC/DC Interface	
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				LA-6801P	Rev 1.0
				Date	Tuesday, January 25, 2011
				Sheet	33 of 61

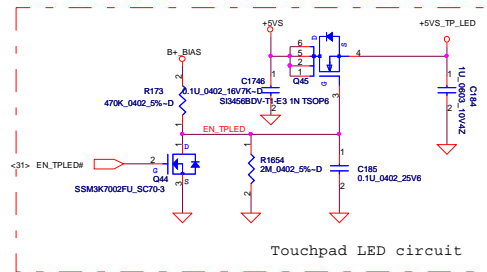


Security Classification	Compal Secret Data		Title	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	ELC (1)/STATUS CONN
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				Document Number
				LA-6801P
				Date: Tuesday, January 25, 2011
				Sheet 34 of 61

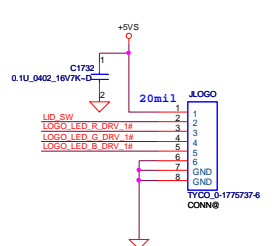
Touchpad LED CONN



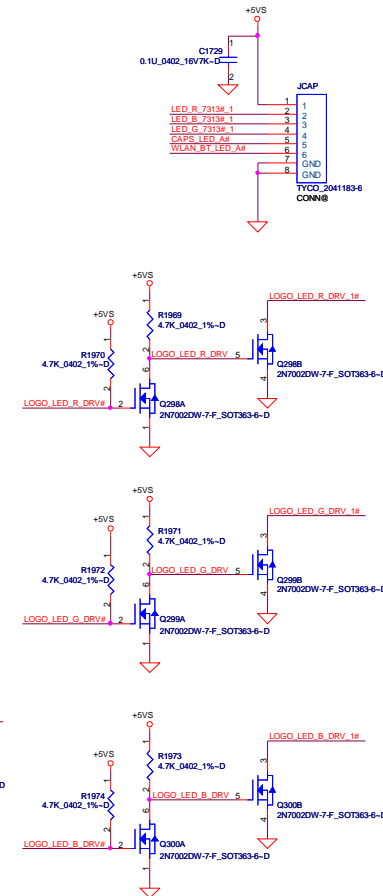
Reference	AD2	AD1	AD0	MAX7313
U605	0	1	0	L/R Headlight , Logo, TP
U608	0	1	1	Num, CAP , SCR EJECT, REV, PLAY/PAUSE FFWD, Vol_DWN, Vol_UP Wireless ON/OFF AWCC Button Alien Adrenaline Power Button Eyes Power Button Rim



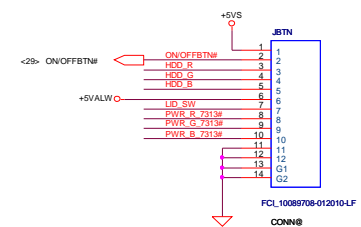
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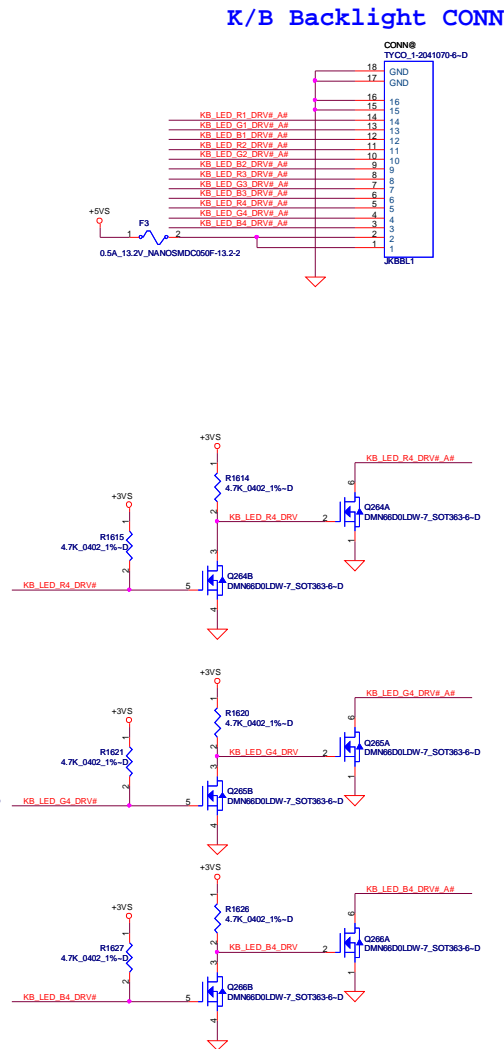
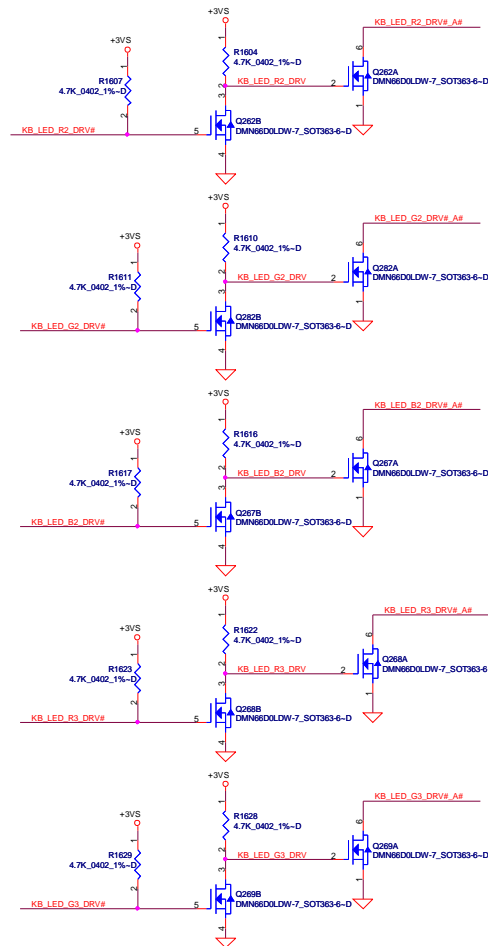
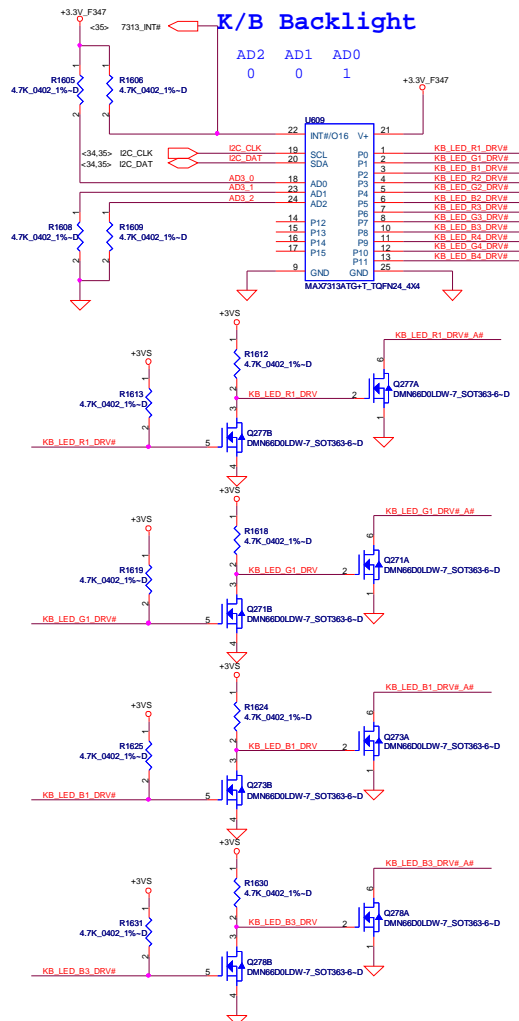
Indicator CONN



PWR BTN Board CONN

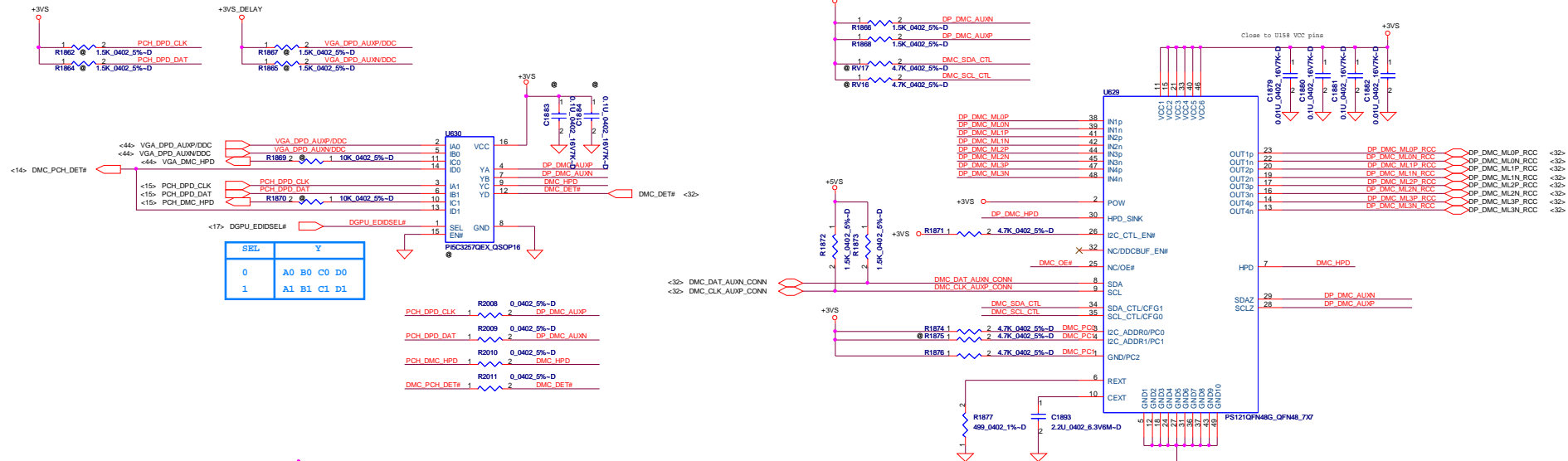


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Issued Date	2011/01/25	Deciphered Date	2012/01/25	ELC (2)
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Date:	Tuesday, January 25, 2011	Sheet	36	of 61

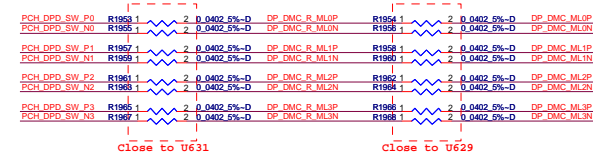


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Issued Date	2011/01/25	Deciphered Date	2012/01/25	ELC (3)
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Date: Tuesday, January 25, 2011				Sheet 36 of 61

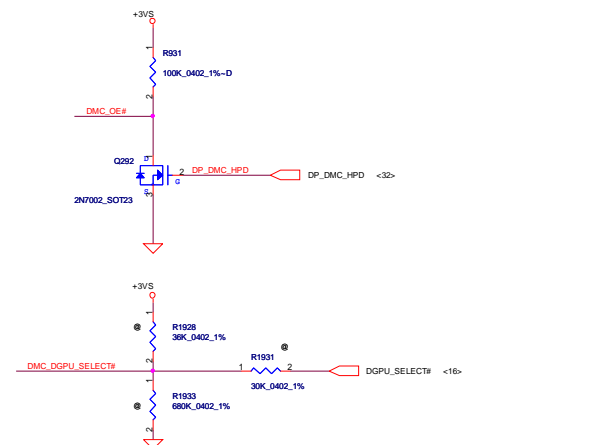
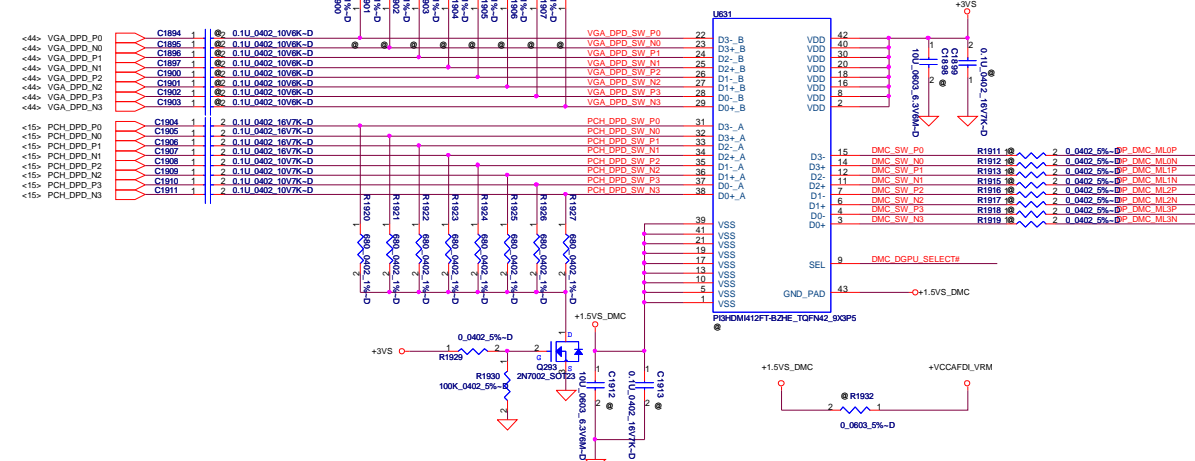
DMC Redriver



PCH/GPU AUX&LANE SW for DMC



6/5 change C188, C1162 0805 to 0603

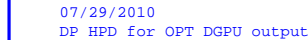


AUX_SEL/SEL1&2	Chanel	Source
0	A	GPU
1	B	PCH

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Size	C			Document Number	Rev
				LA-6801P	1.0
Date	Transferred	Deciphered	2011/01/25	Sheet	37 of 61



Normal



Compal Electronics, Inc.

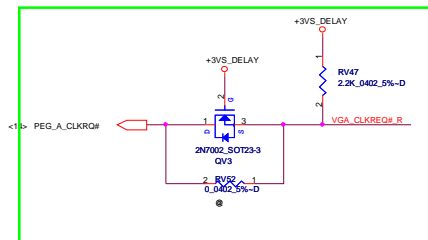
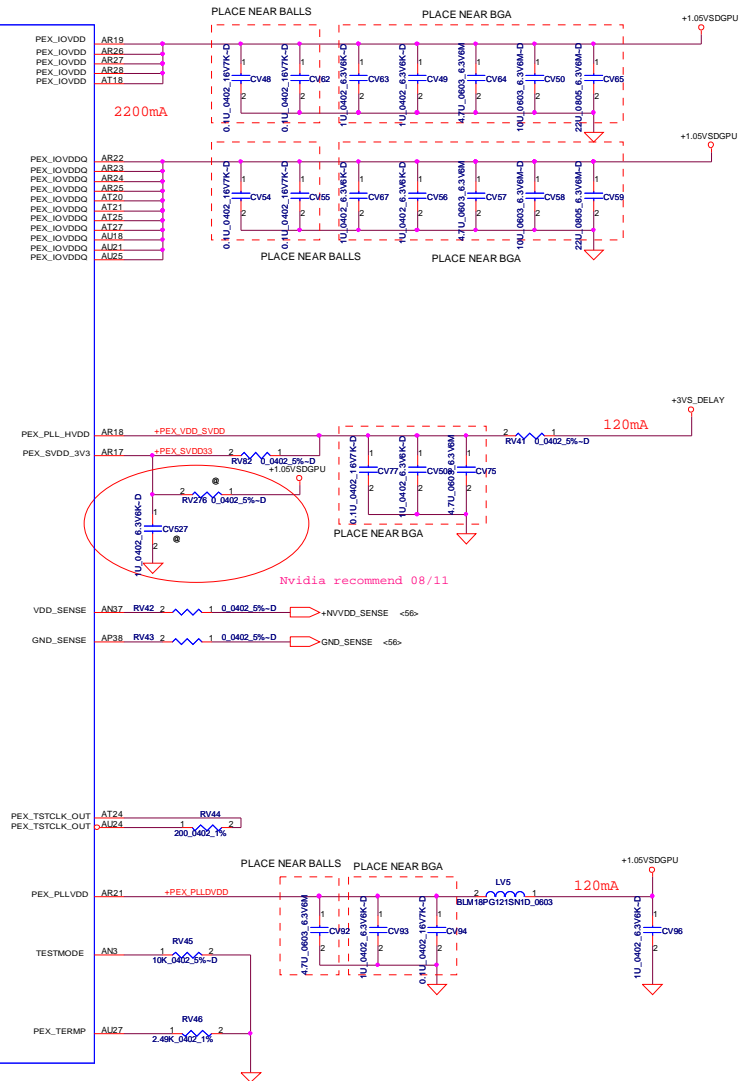
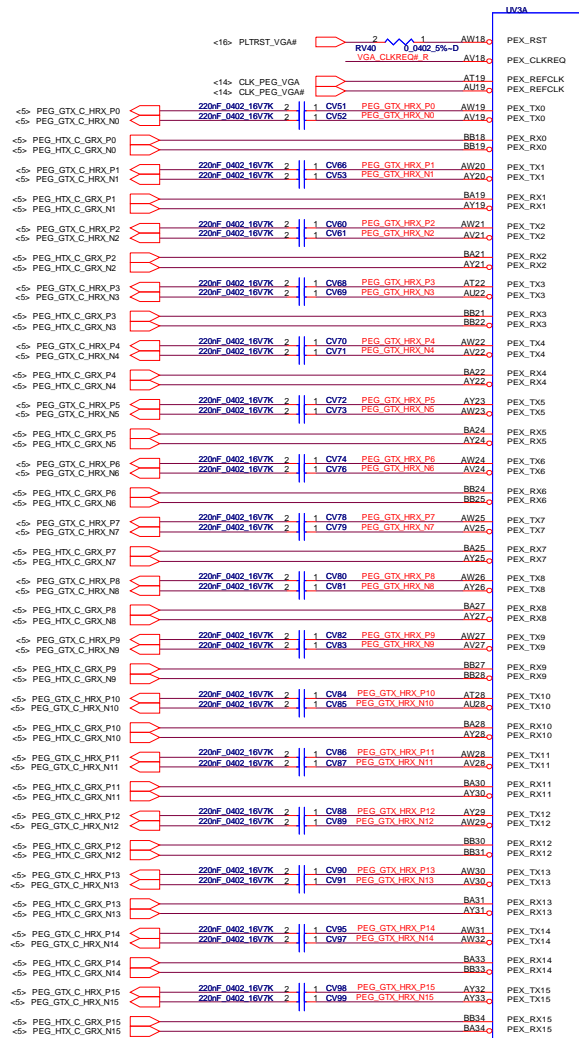
Mini Display Port

Size	Document Number
Custom	LA-6801P

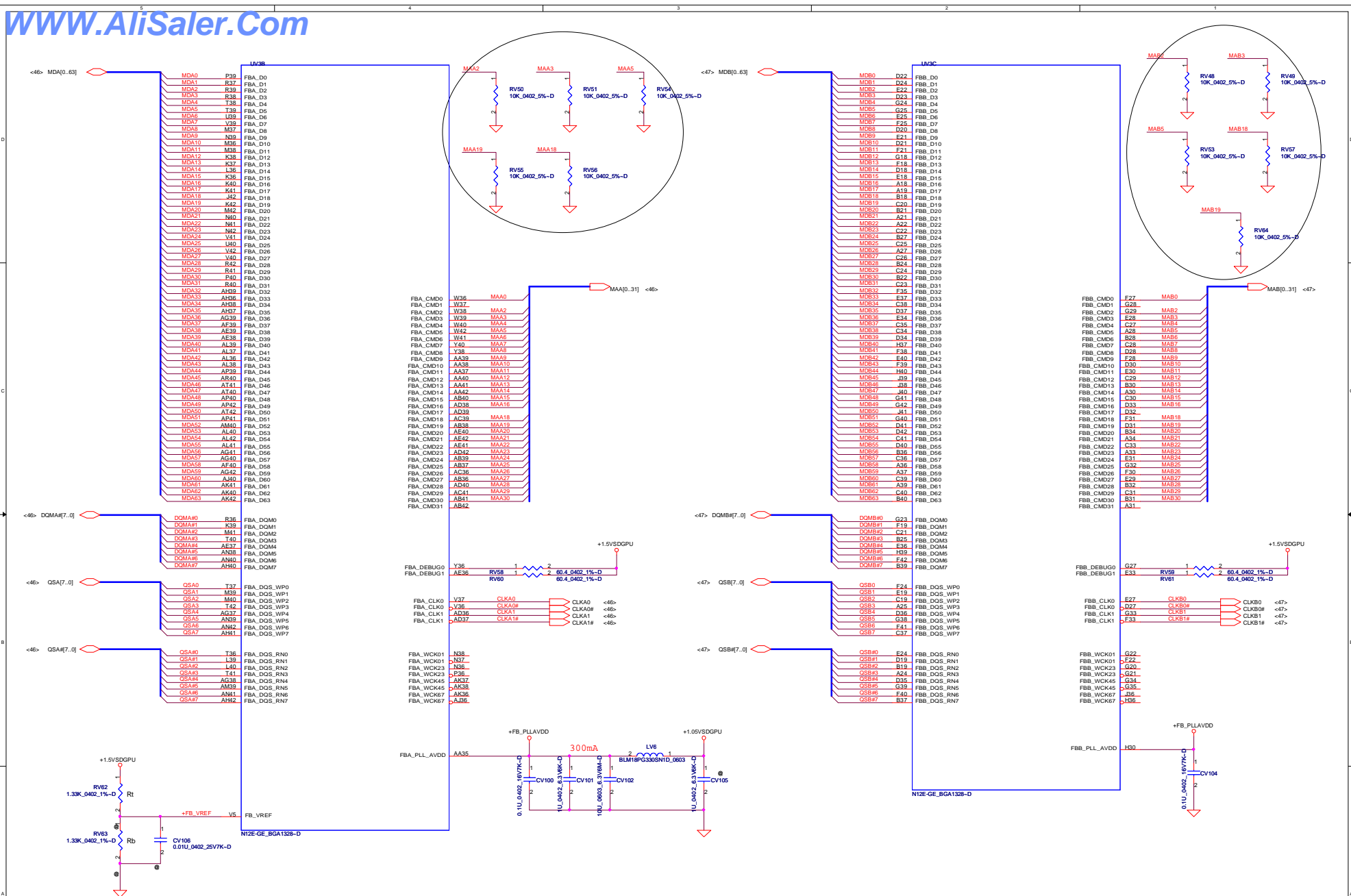
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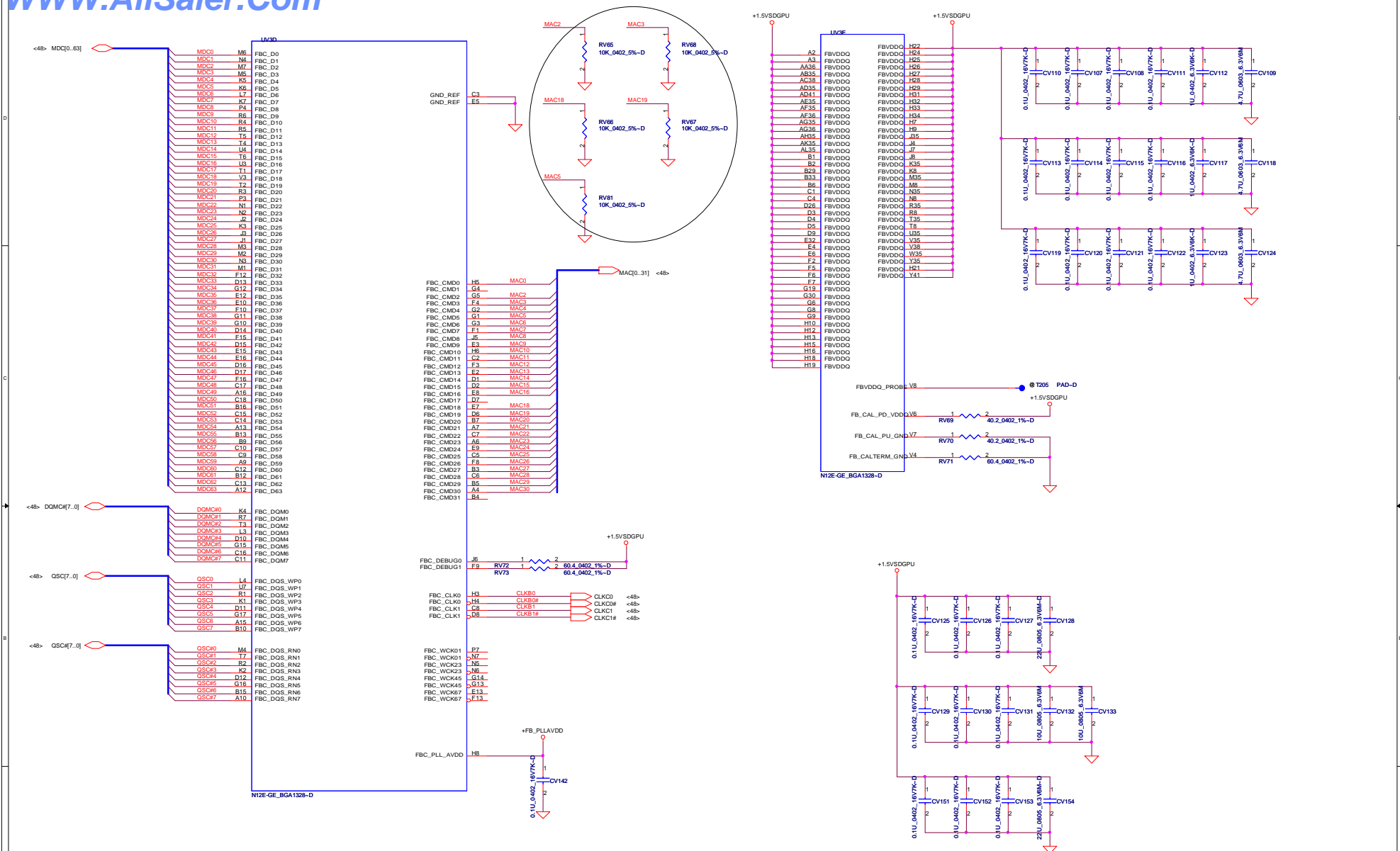
Date: Tuesday, January 25, 2011 Sheet 38 of 61



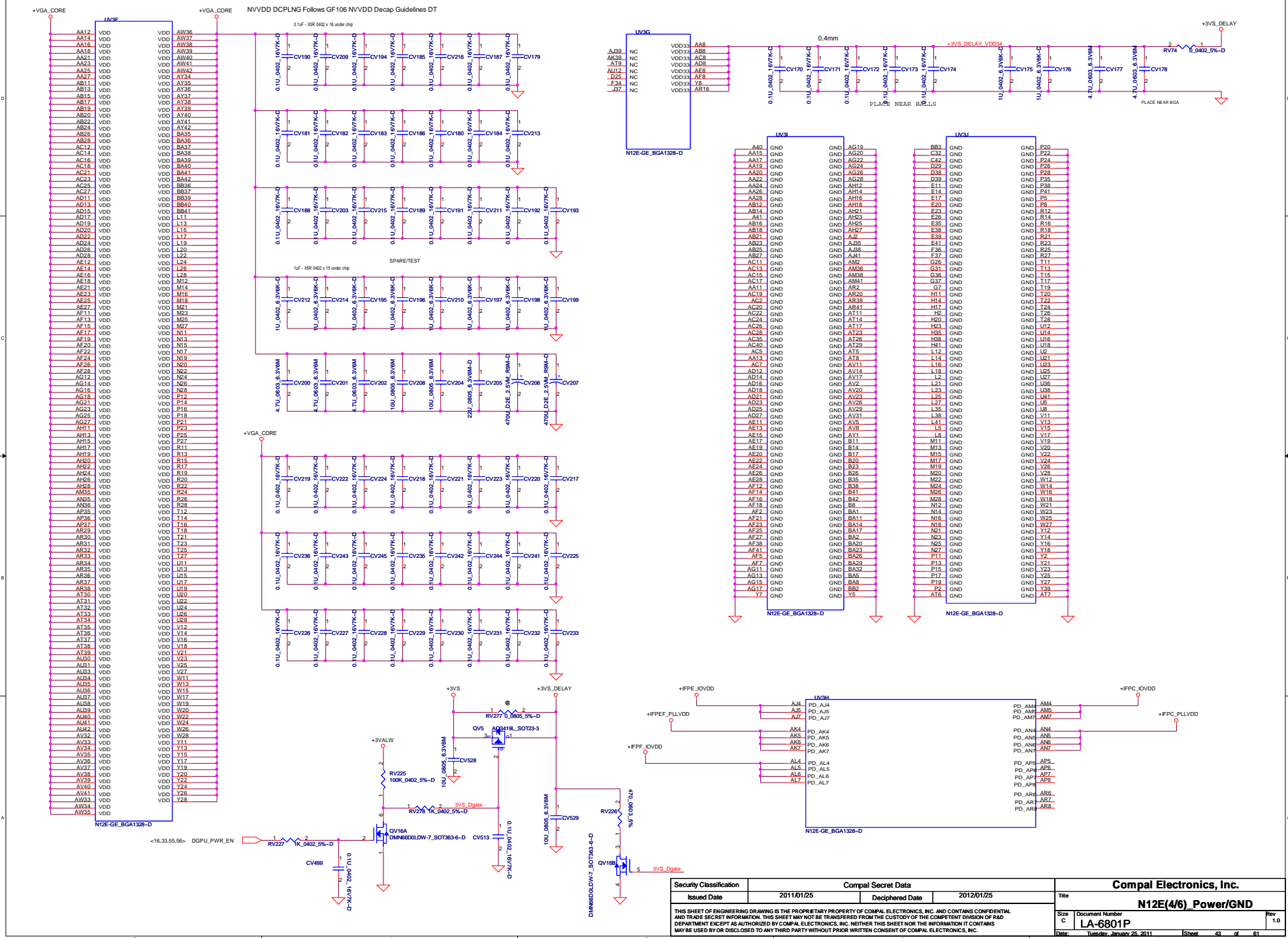


Security Classification		Compal Secret Data		Title	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	N12E(1/6) PCIE	
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				C	1.0
				LA-6801P	
				Date: Tuesday, January 25, 2011	Sheet 40 of 61



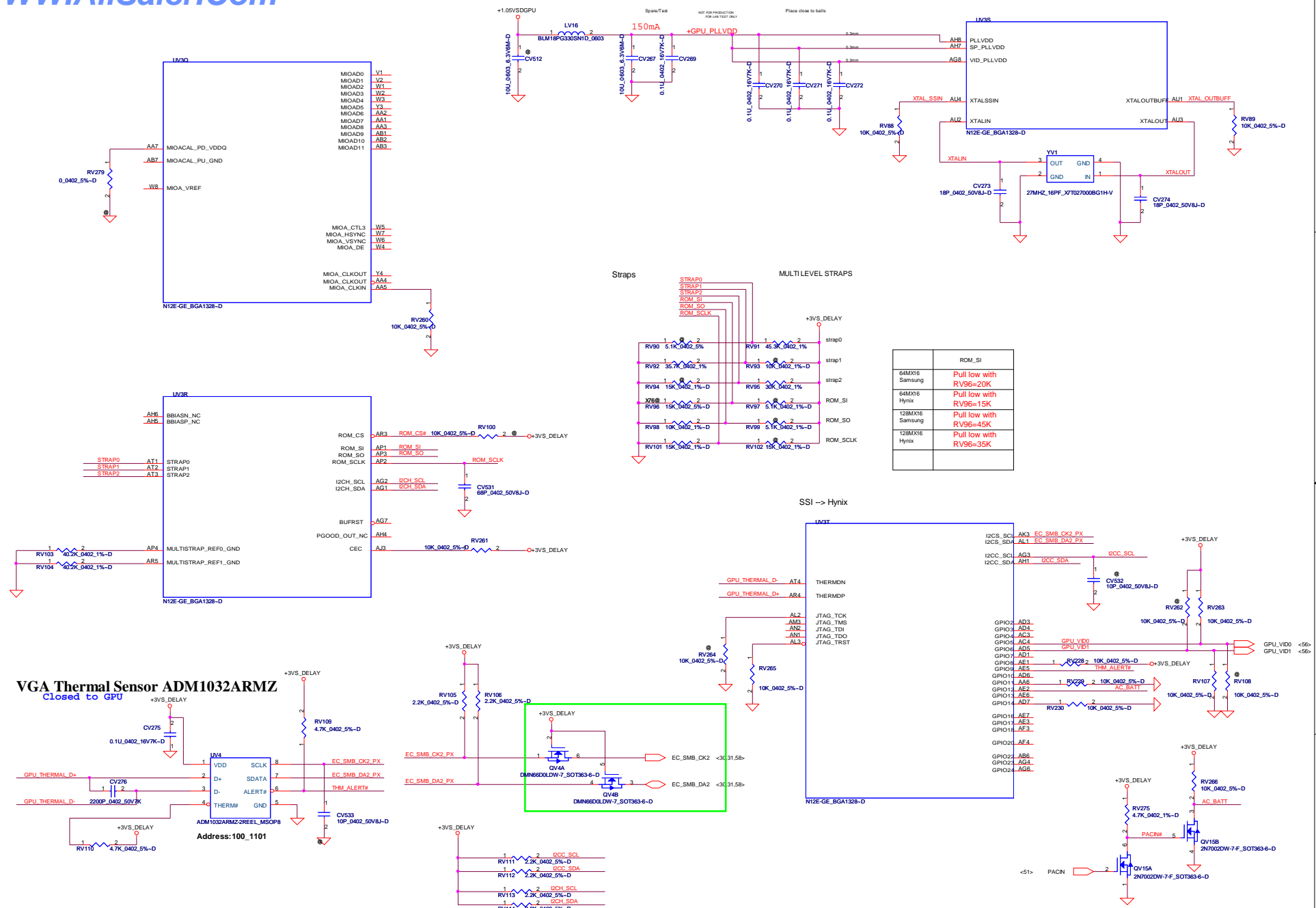


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	N12E(3/6) MemoryC	
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				C	LA-6801P	1.0
				Date	Tuesday, January 25, 2011	Sheet 42 of 61



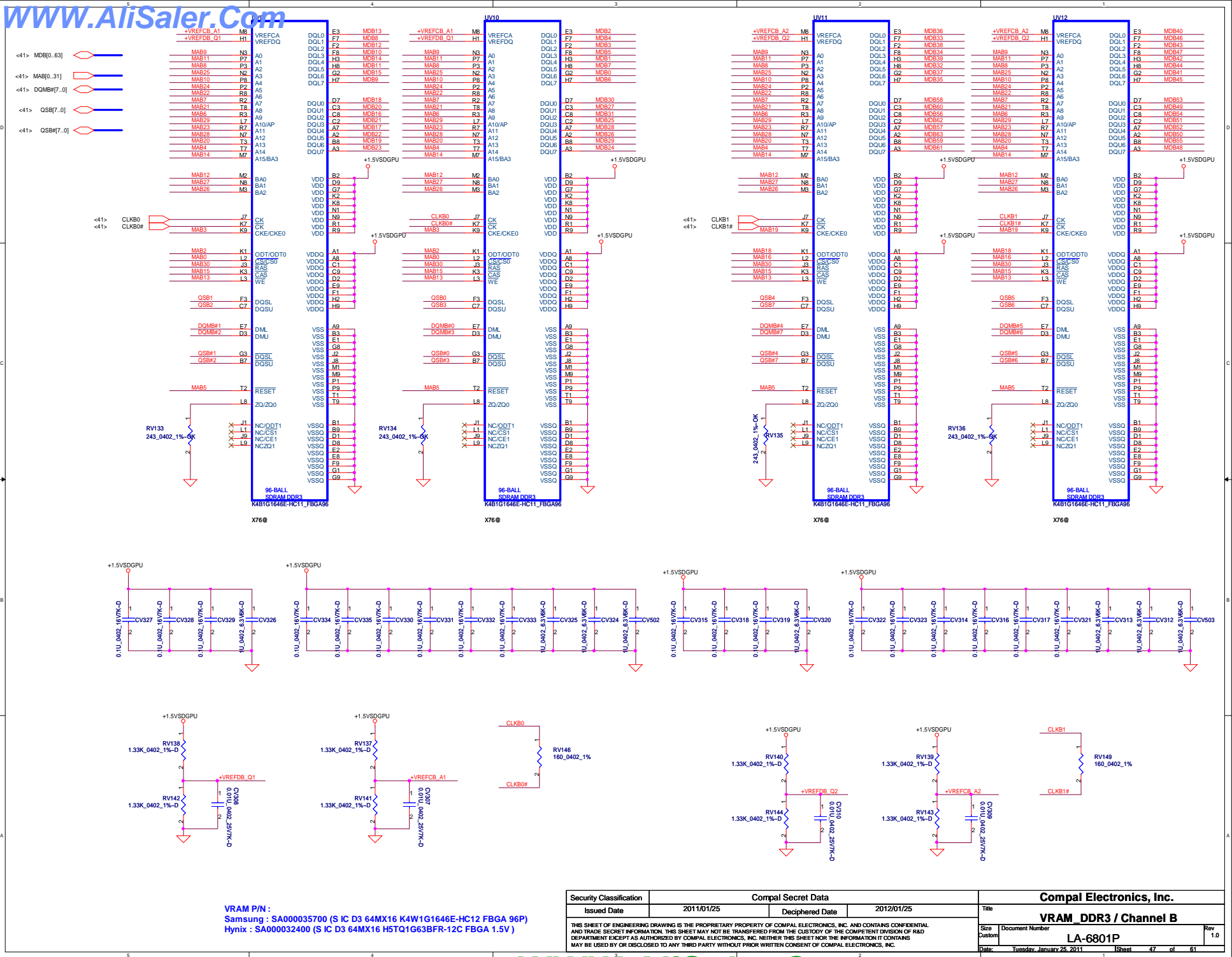


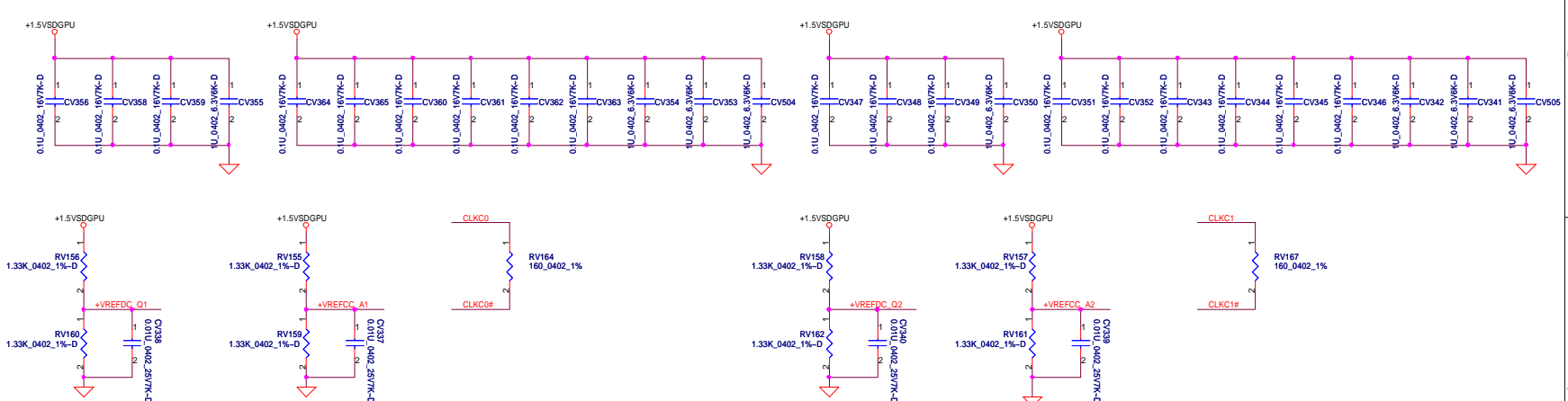
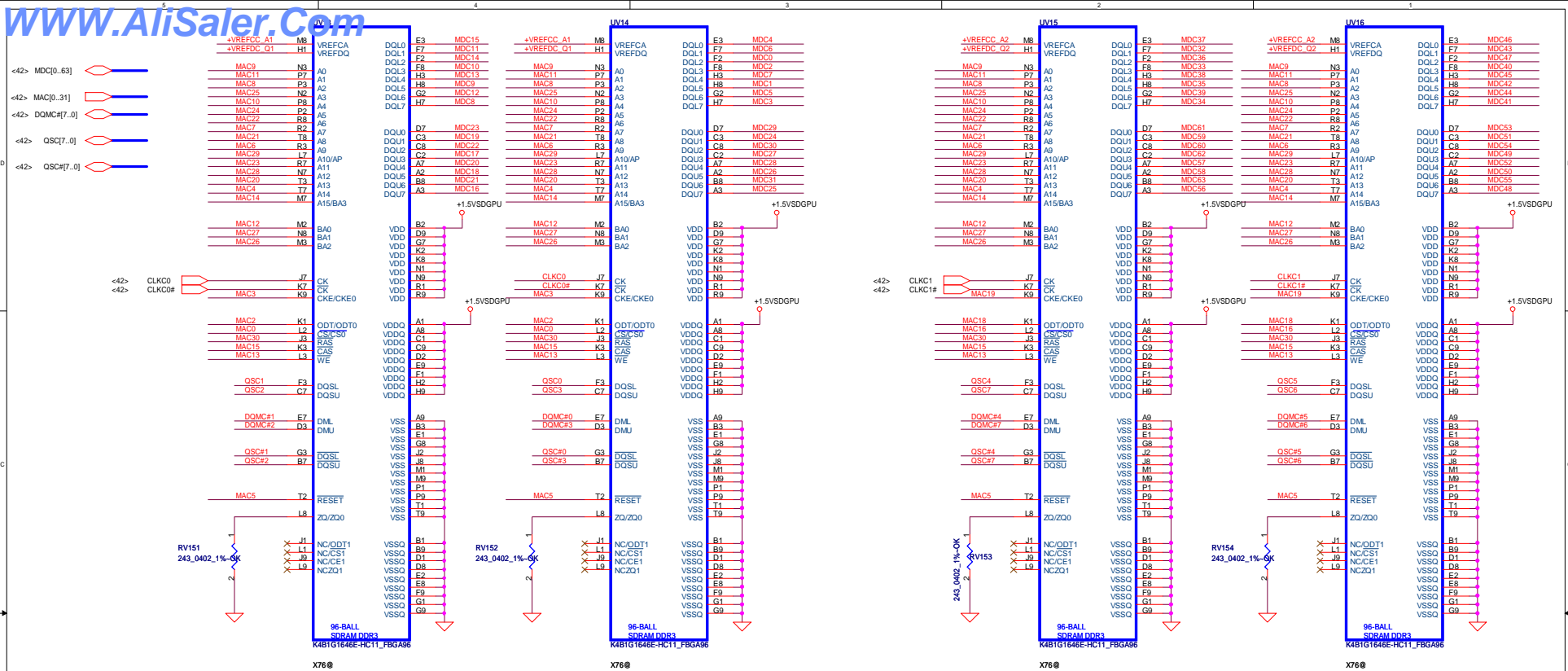
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Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	N12E(6/6) GPIO	
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				C	LA-6801P	1.0
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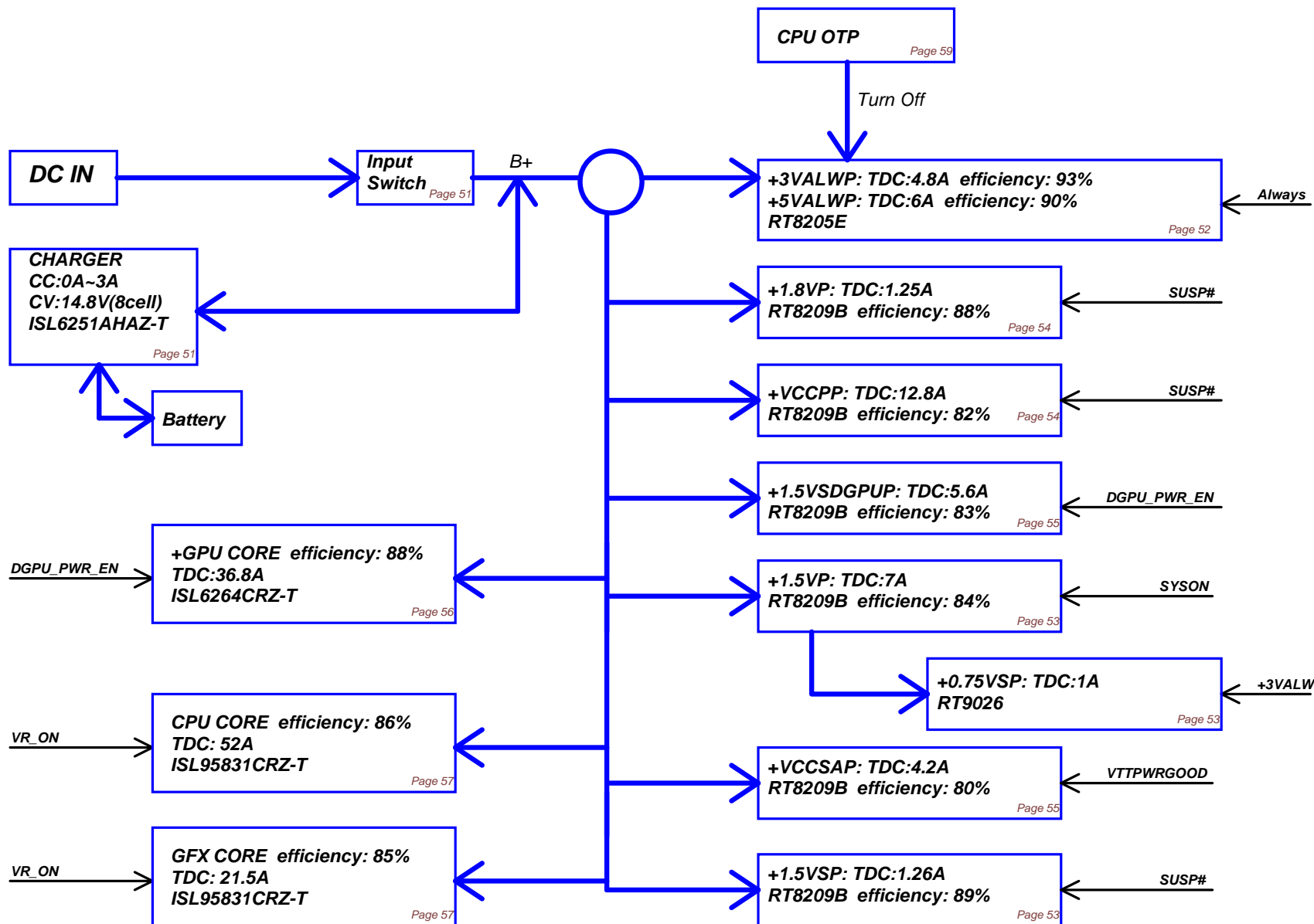




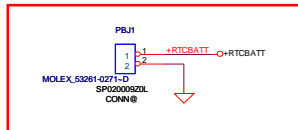
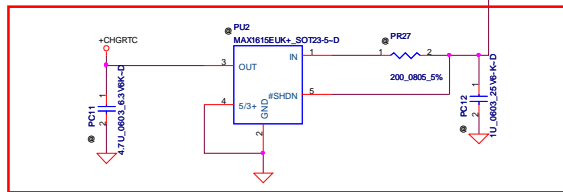
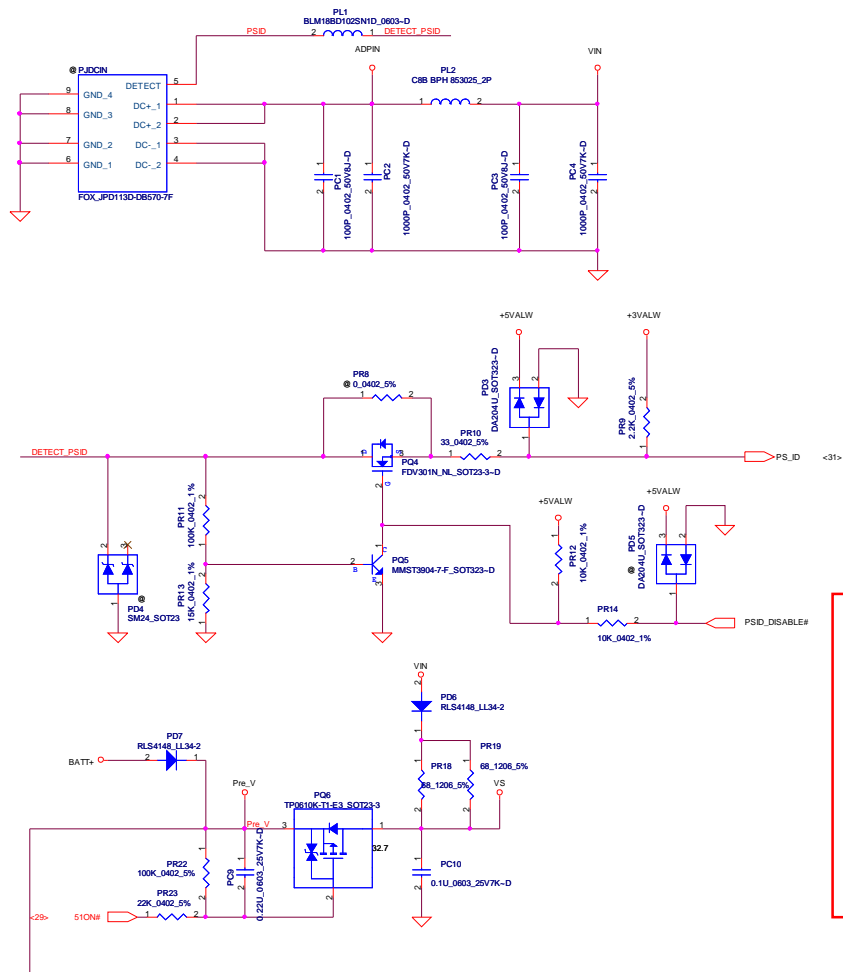
VRAM P/N :
Samsung : SA000035700 (S IC D3 64MX16 K4W1G1646E-HC12 FBGA 96P)
Hynix : SA000032400 (S IC D3 64MX16 H5TQ1G63BFR-12C FBGA 1.5V)

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Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	VRAM_DDR3 / Channel C
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				Date	Tuesday, January 25, 2011
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Power block

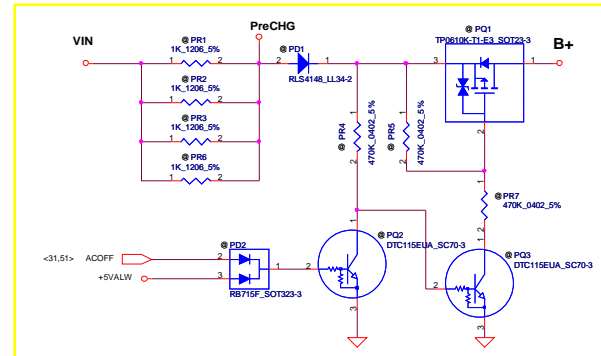


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	POWER BLOCK DIAGRAM
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				Document Number	LA-6801P
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				Sheet	49 of 61
				Rev	1.0

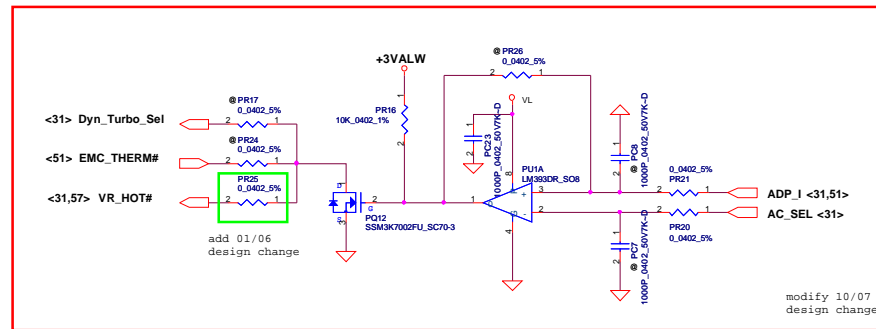


revise 9/17
reduce S5 loss

modify 10/21
for ME request



del 12/13
design change



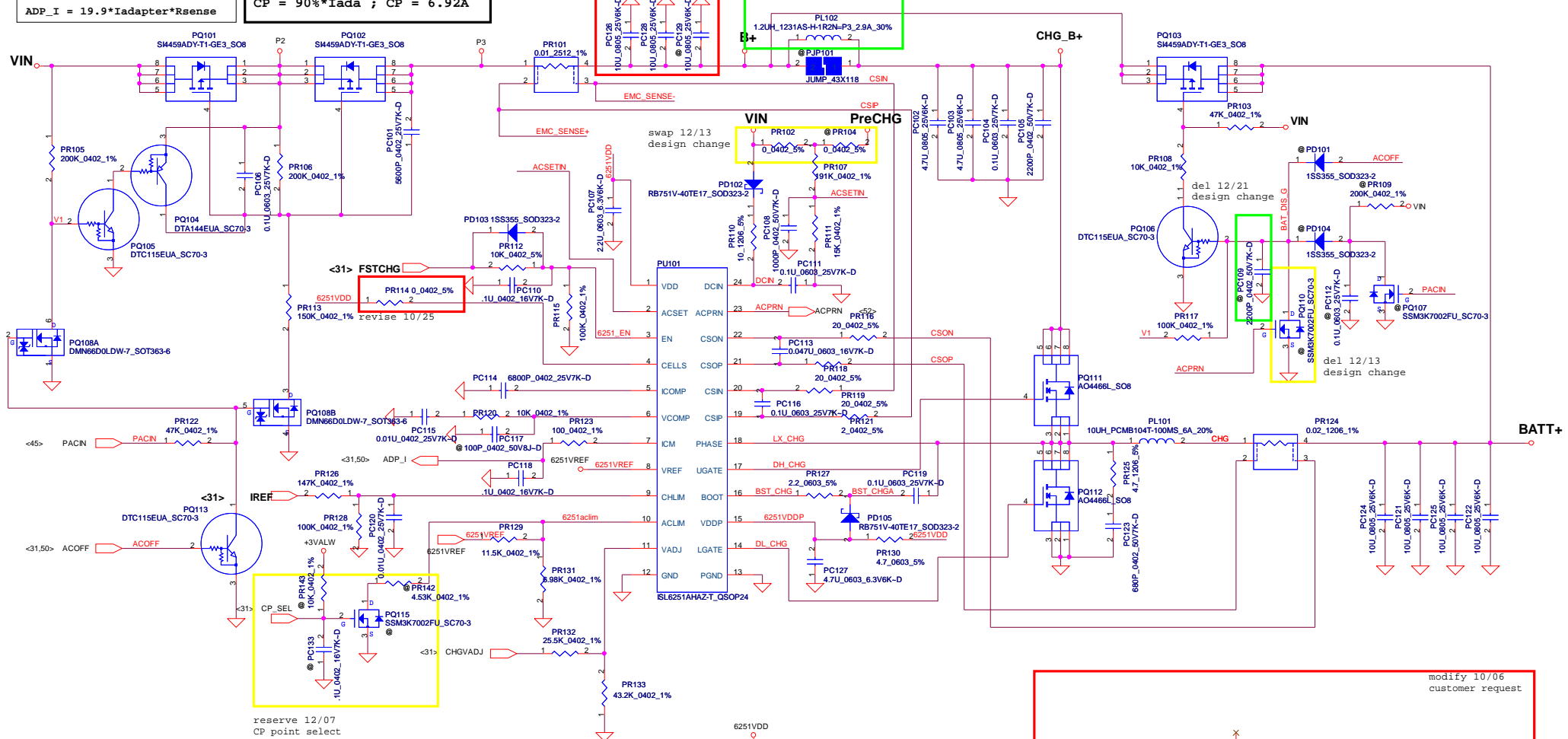
modify 10/07
design change

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	PWR-DCIN / Vin Detector
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				Sheet	60 of 61

CP = 90%*Iada ; CP = 6.92A

add 10/19
for EMI request

add 01/20
for EMI request

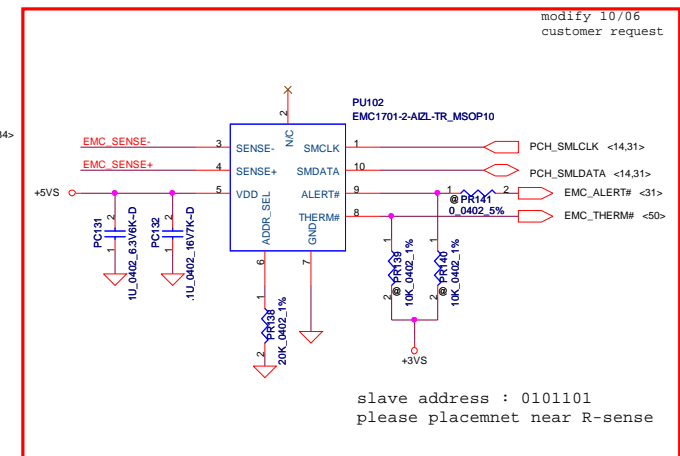


CP mode
 $I_{input} = (1/0.02) * (0.05 * V_{ac1m} / 2.39 + 0.05)$
 $V_{ac1m} = 2.39 * ((6.98K / 152K) / ((11.5K / 152K) + (6.98K / 152K)))$

CC=3.3A
 IREF=I*Charge
 IREF=0.25V~3.3V

BATT Type	Charging Voltage (0x15)	CV mode
Normal 4S LI-ON Cells	14800mV	14.80V

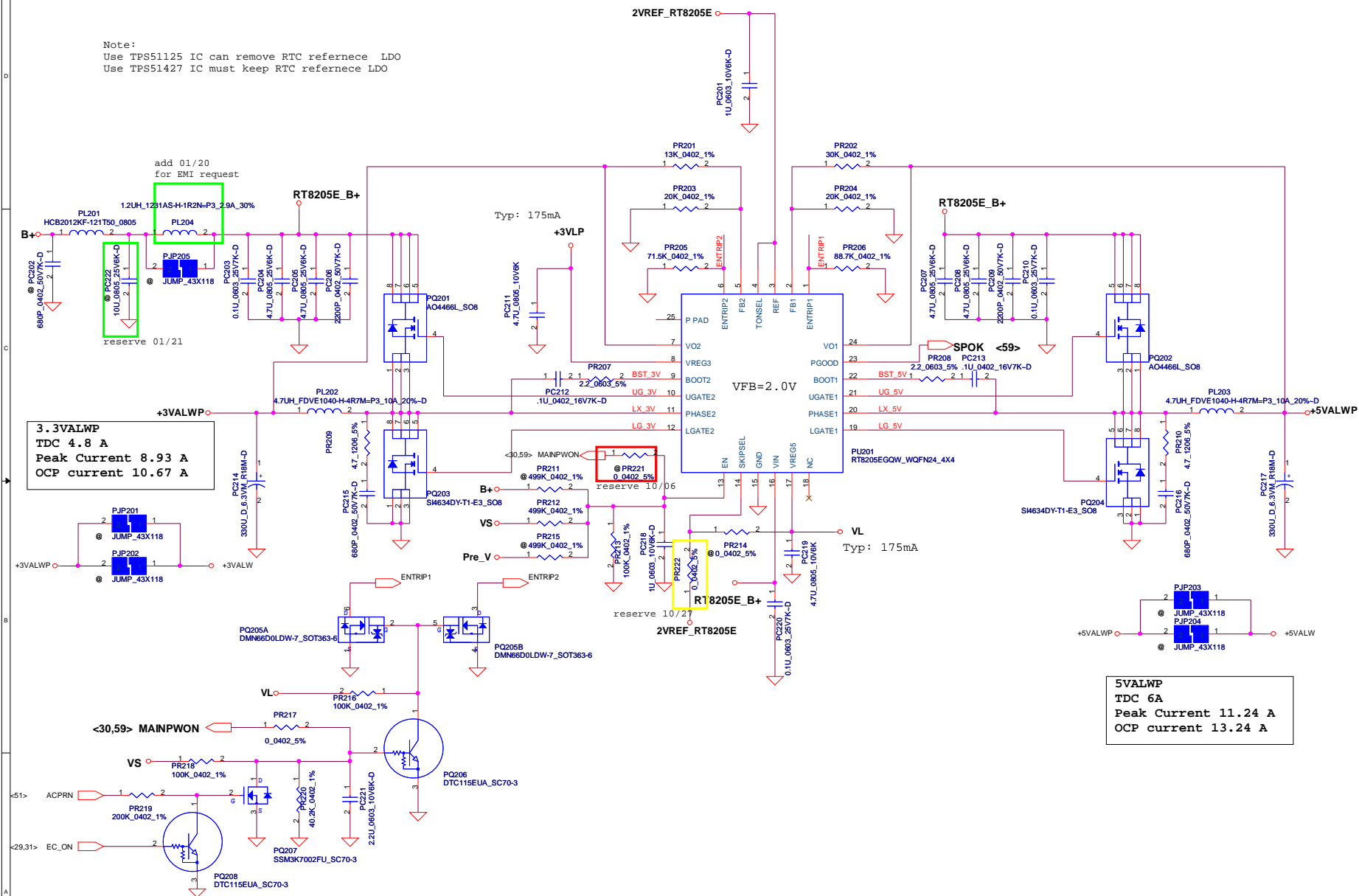
CHGVADJ	CV mode
0V	3.99V per cell
1.93V	4.2V per cell
3.3V	4.35V per cell



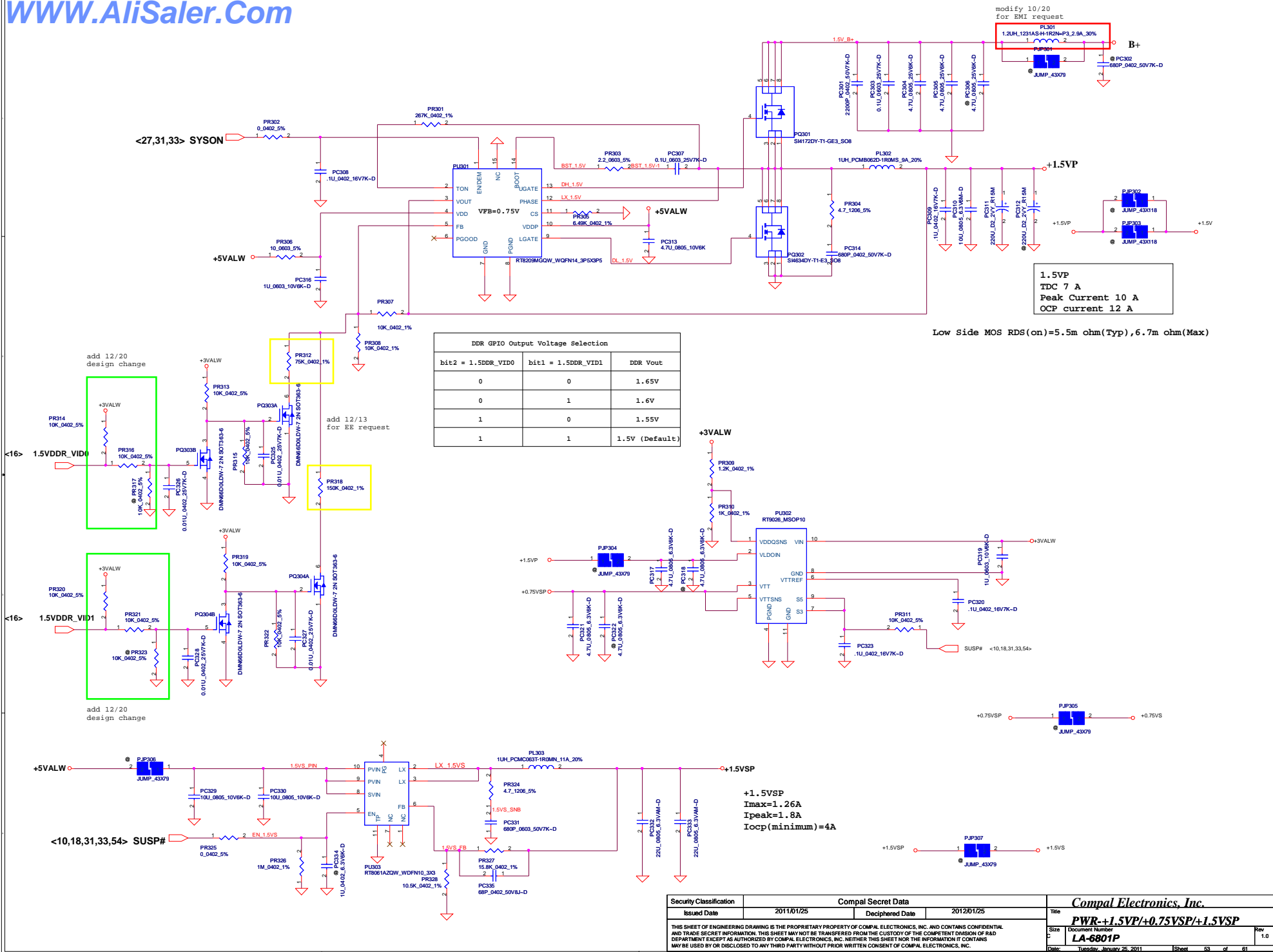
slave address : 0101101
 please placemnet near R-sense

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Issued Date		2011/01/25	Deciphered Date		2012/01/25
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				PWR-CHARGER	
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Date:		Tuesday, January 25, 2011		Sheet	51 of 61

Note:
Use TPS51125 IC can remove RTC refernece LDO
Use TPS51427 IC must keep RTC refernece LDO

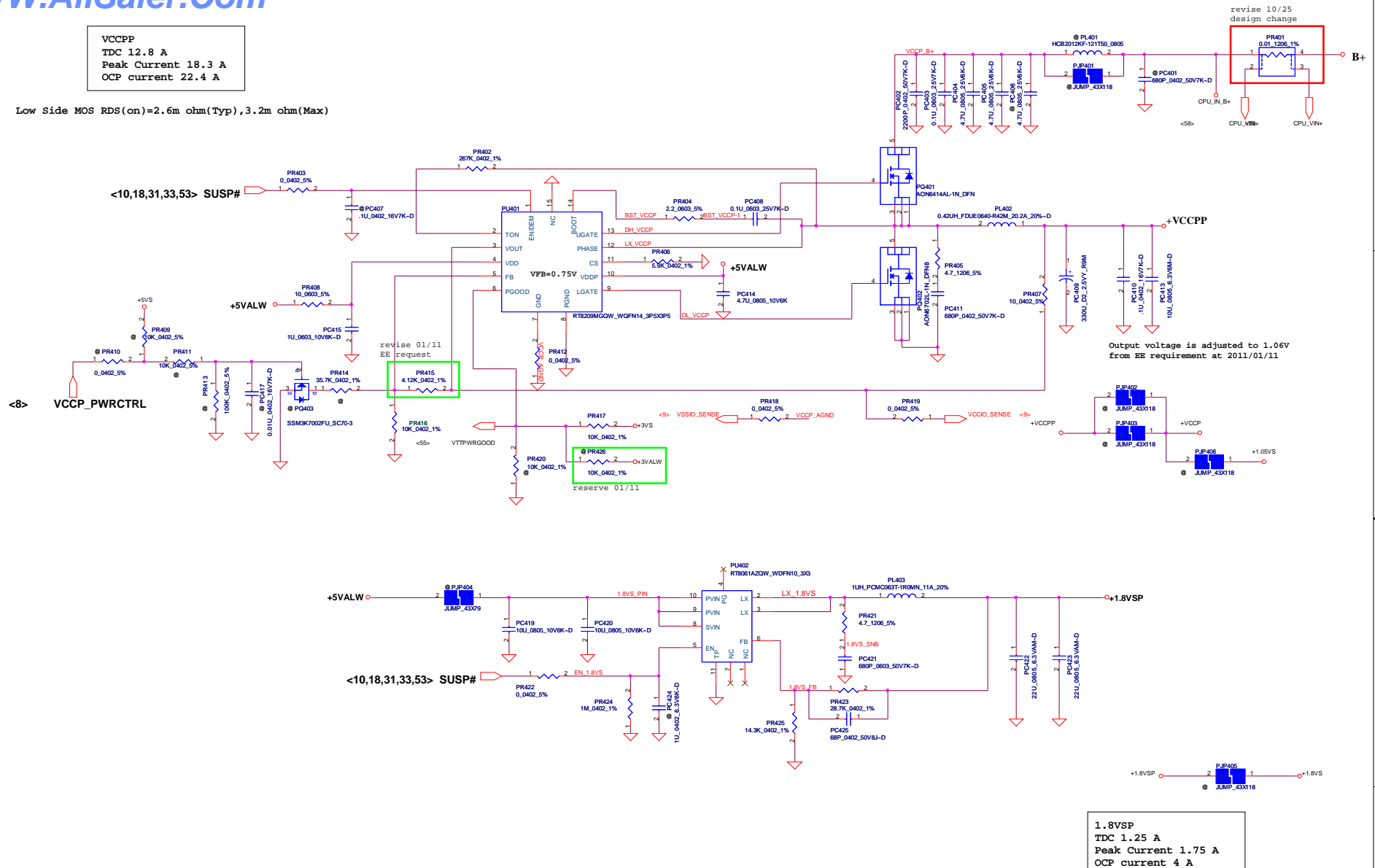


Security Classification		Compal Secret Data		<i>Compal Electronics, Inc.</i> 3VALWP/SVALWP	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	3VALWP/SVALWP LA-6801P
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				Date:	Tuesday, January 25, 2011
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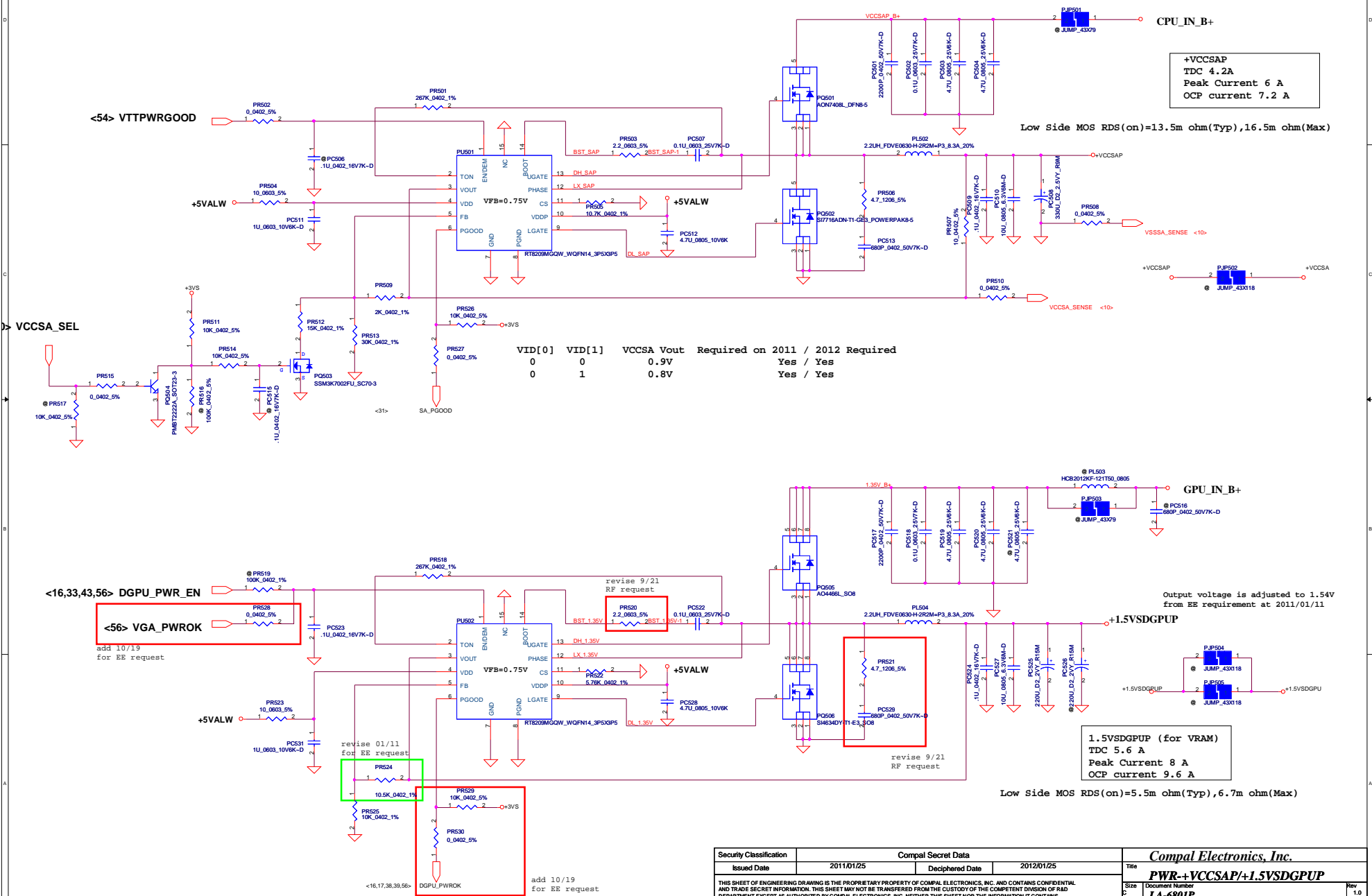


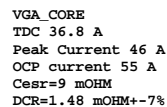
VCCPP
TDC 12.8 A
Peak Current 18.3 A
OCP current 22.4 A


Low Side MOS RDS(on)=2.6m ohm(Typ), 3.2m ohm(Max)

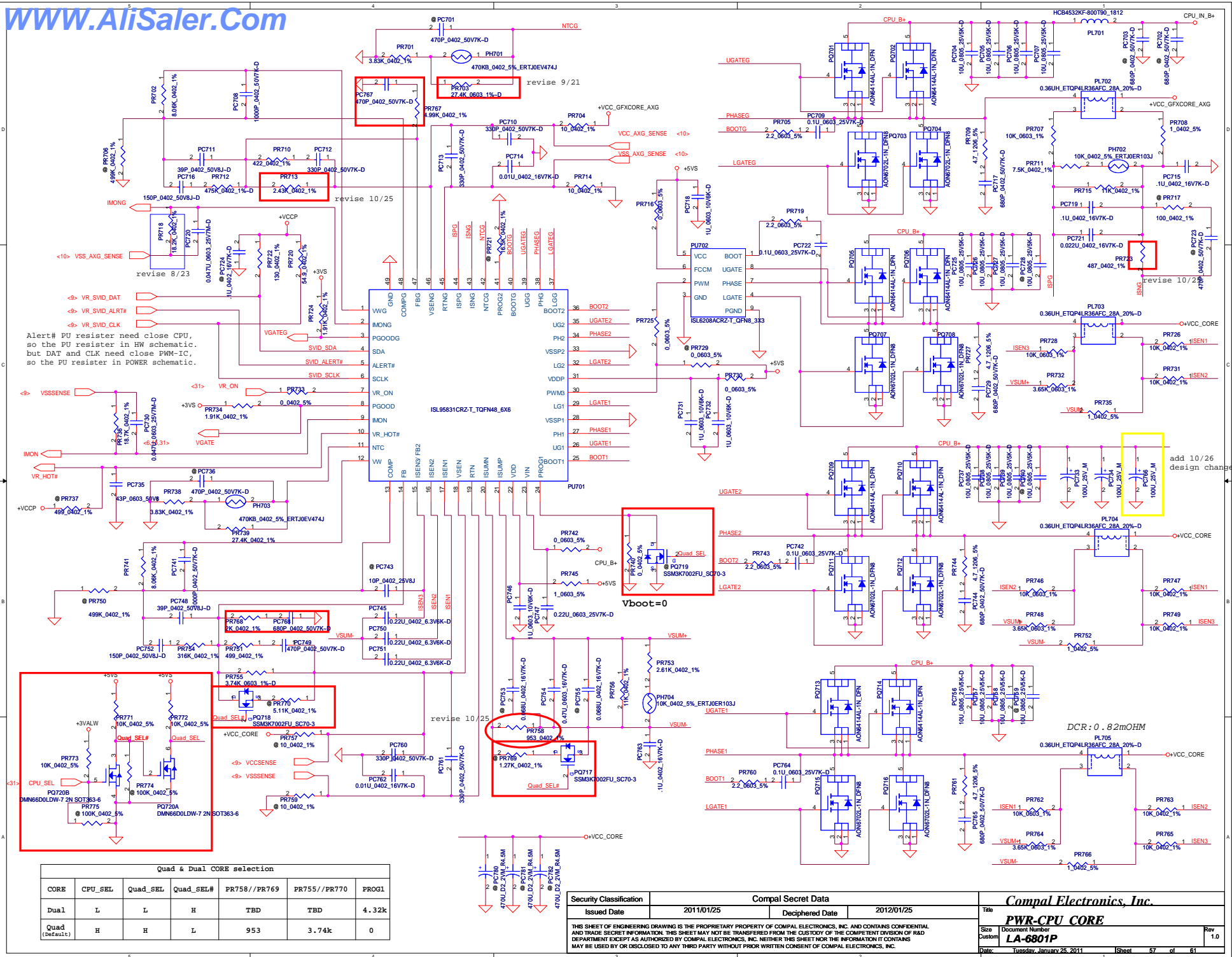


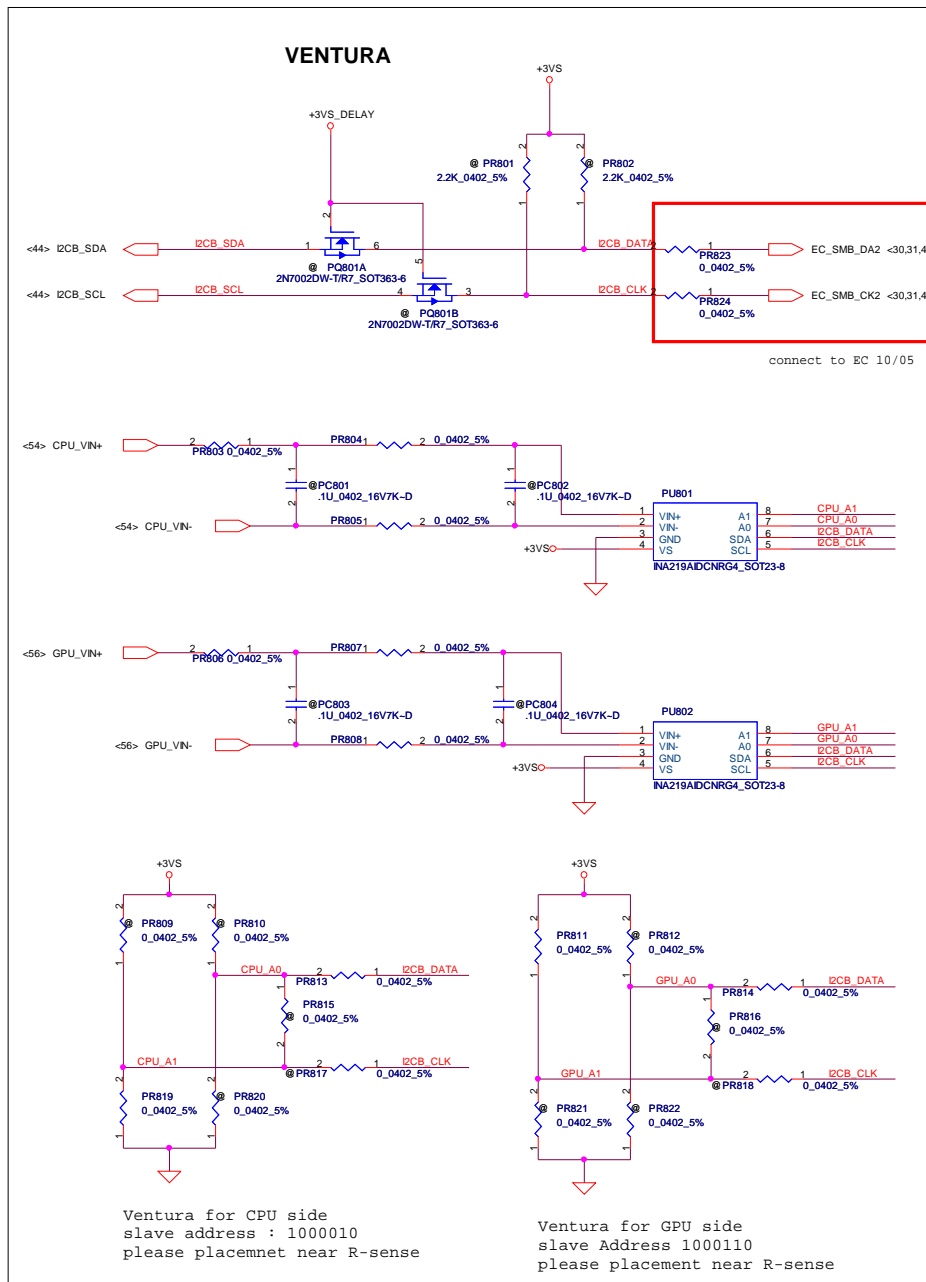
Security Classification		Compal Secret Data		Title	
Issued Date	2011/01/25	Deciphered Date	2012/01/25	Compal Electronics, Inc.	
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				Size	Document Number
				C	LA-6301P
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			DCA CORE Document Number LA-6801P	
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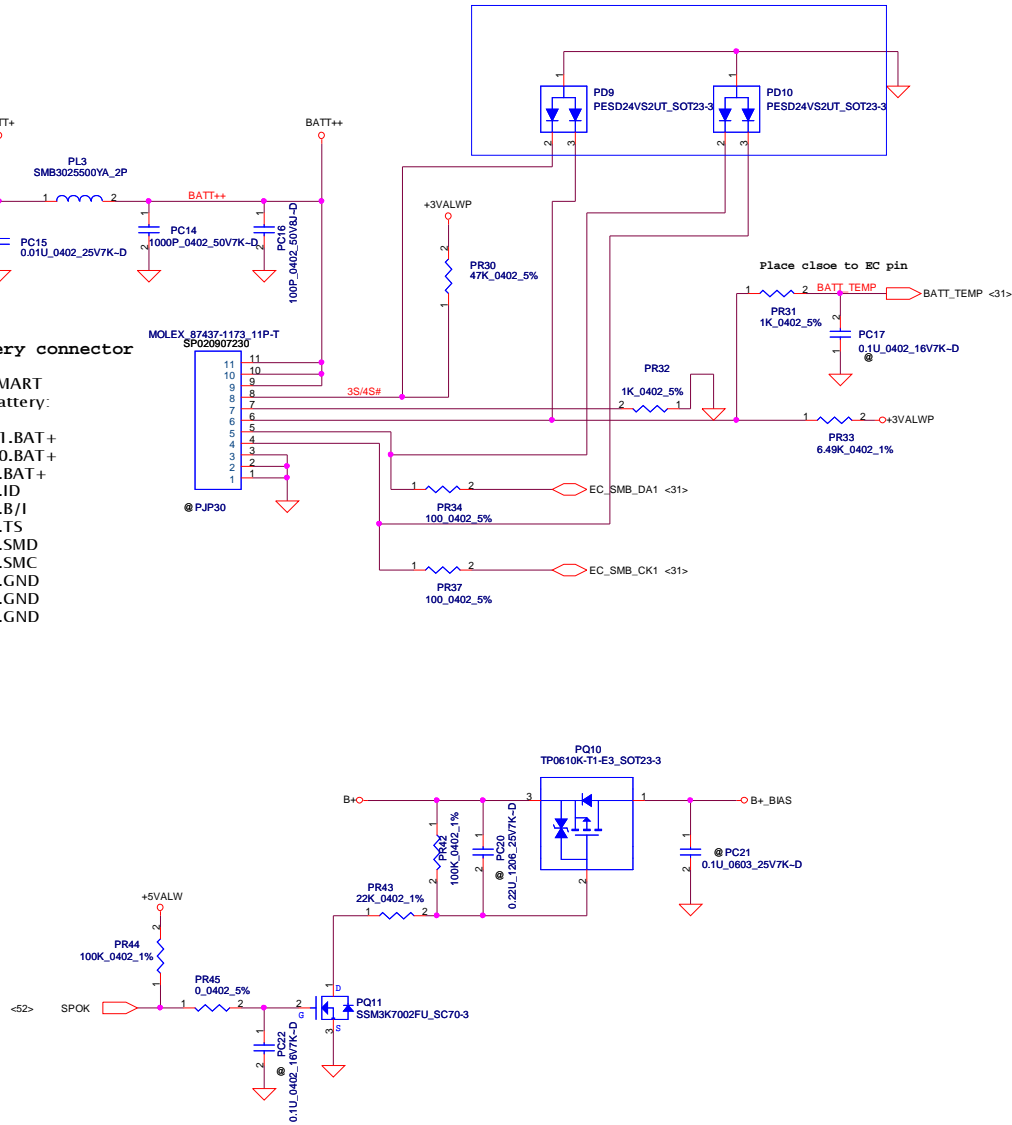
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Issued Date	2011/01/25	Deciphered Date	2012/01/25	Title	VENTURA
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revise 8/09
ESD request

PJP30 battery connector

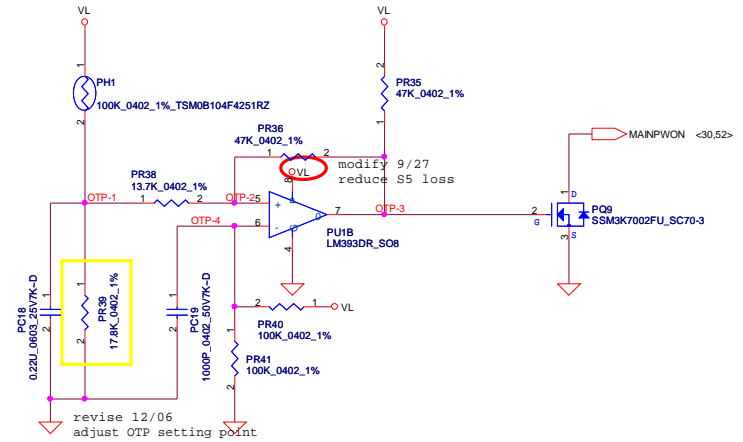
SMART
Battery:

- 11.BAT+
- 10.BAT+
- 9.BAT+
- 8.ID
- 7.B/I
- 6.TS
- 5.SMD
- 4.SMC
- 3.GND
- 2.GND
- 1.GND



Battery Connect/OTP

PH1 under CPU bottom side :
CPU thermal protection at 90 degree C
Recovery at 50 degree C



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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	50	PWR-DCIN / Vin Detector	10/09/17	COMPAL	reduce SS loss	add PU2;PR27;PC11;PC12	0.2
2	55	+VCCSAP/+1.5VSDGUP	10/09/21	COMPAL	RF request	add PR521;PC529 change PR520 from 0 to 2.2	0.2
3	57	+CPU_CORE	10/09/21	COMPAL	design change	change PR703 from 0 to 27.4K	0.2
4	51	CHARGER	10/10/06	COMPAL	design change	change PC131 from 0.1u to 1u	0.2
5	50	PWR-DCIN / Vin Detector	10/10/07	COMPAL	design change	add PQ12;PC23	0.2
6	54	+VCCPP/+1.8VSP	10/10/07	COMPAL	design change	modify 1.8VSP solution	0.2
7	51	CHARGER	10/10/19	COMPAL	EMI request	add PC126;PC128	0.2
8	55	+VCCSAP/+1.5VSDGUP	10/10/19	COMPAL	EE request	add PR528;PR529;PR530 del PR519	0.2
9	56	+VGA_CORE	10/10/19	COMPAL	EE request	add PR647;PR648 del PR603;PR604	0.2
10	51	CHARGER	10/10/25	COMPAL	design change	change PR114 from 47K to 0	0.2
11	54	+VCCPP/+1.8VSP	10/10/25	COMPAL	design change for use ventura curcuit	change PR401 from 0 to 0.01	0.2
12	56	+VGA_CORE	10/10/25	COMPAL	design change for use ventura curcuit	change PR601 from 0 to 0.01	0.2
13	57	+CPU_CORE	10/10/25	COMPAL	change CPU OCP setting	change PR758 from 887 to 953	0.2
14	57	+CPU_CORE	10/10/25	COMPAL	change GFX OCP & LL setting	change PR723 from 442 to 487 change PR713 from 2.61K to 2.43K	0.2
15	57	+CPU_CORE	10/10/25	COMPAL	quad & dual core CPU switch setting design	add PQ717;PQ718;PQ720;PR771;PR772;PR773;PR767;PR768;PC767;PC768	0.2
16	58	VENTURA	10/10/25	COMPAL	add ventura connect to EC	add PU801;PU802;PR803;PR804;PR805;PR806;PR807;PR808	0.2
						;PR811;PR813;PR814;PR819;PR823;PR824	0.2
17	57	+CPU_CORE	10/10/26	COMPAL	design change for solve acoustic issue	add PC766	0.3
18	52	3VALWP/5VALWP	10/10/27	COMPAL	design change for reserve adjust working frequency	add PR222	0.3
19	59	BATTERY CONN	10/12/06	COMPAL	adjust OTP setting point	change PR39 from 16.9K to 17.8K	0.3
20	50	PWR-DCIN / Vin Detector	10/12/13	COMPAL	design change for delete prechange circuit	del PR1;PR2;PR3;PR4;PR5;PR6;PR7;PD1;PD2;PQ1;PQ2;PQ3	0.3
21	51	CHARGER	10/12/13	COMPAL	design change for delete prechange circuit	del PR104;PQ110 add PR102	0.3
22	53	+1.5VP/+0.75VSP/+1.5VSP	10/12/13	COMPAL	EE request for use memory over clocking circuit	add PR312;PR318	0.3
23	53	+1.5VP/+0.75VSP/+1.5VSP	10/12/20	COMPAL	design change for adjust original output voltage	swap PR314;PR317 and PR320;PR323	0.4
24	51	CHARGER	10/12/21	COMPAL	design change for delete prechange circuit	del PC109	0.4
25	50	PWR-DCIN / Vin Detector	11/01/06	COMPAL	design change for add AC peak power function	add PR25	0.4
26	54	+VCCPP/+1.8VSP	11/01/11	COMPAL	EE request for adjust output voltage	change PR415 from 4.02K to 4.12K	0.4
27	55	+VCCSAP/+1.5VSDGUP	11/01/11	COMPAL	EE request for adjust output voltage	change PR524 from 10K to 10.5K	0.4
28	51	CHARGER	11/01/20	COMPAL	EMI request	add PL102	0.4
29	52	3VALWP/5VALWP	11/01/20	COMPAL	EMI request	add PL204	0.4

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